

FIG. 1
PRIOR ART

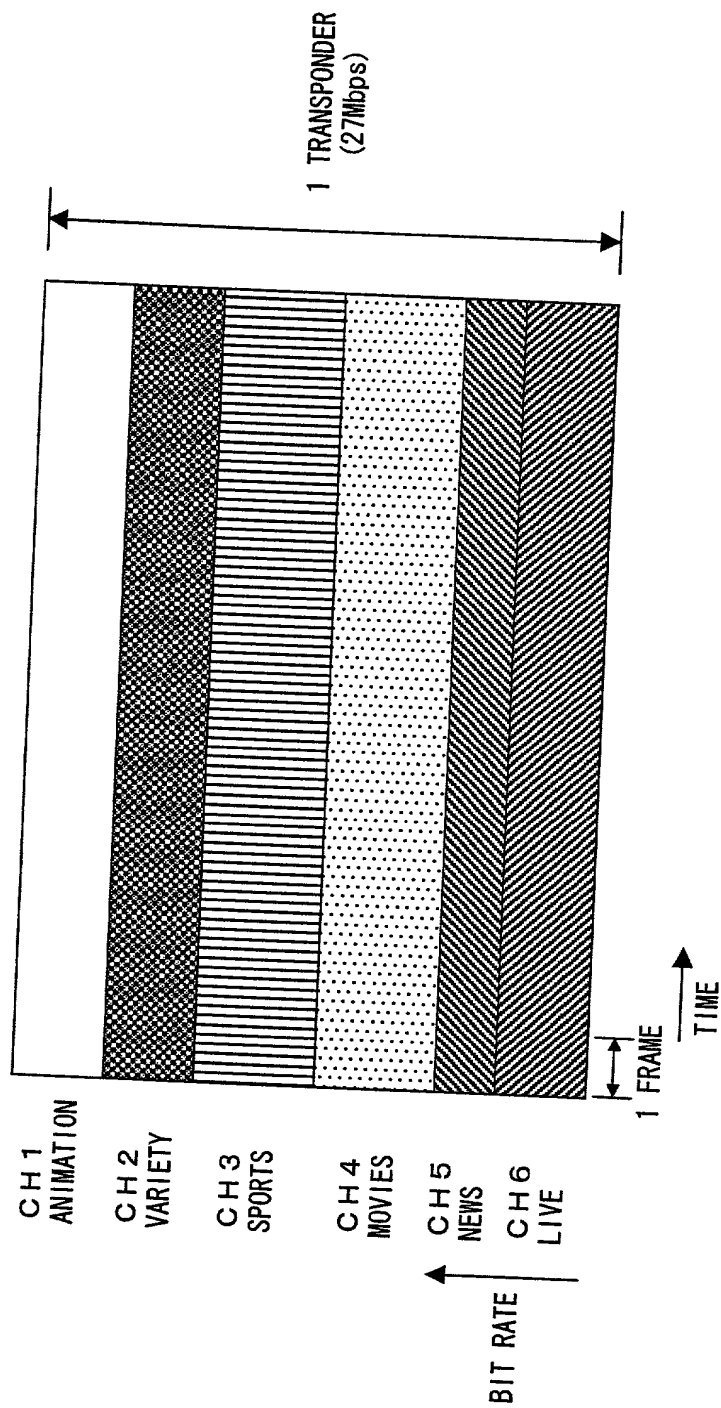


FIG. 2

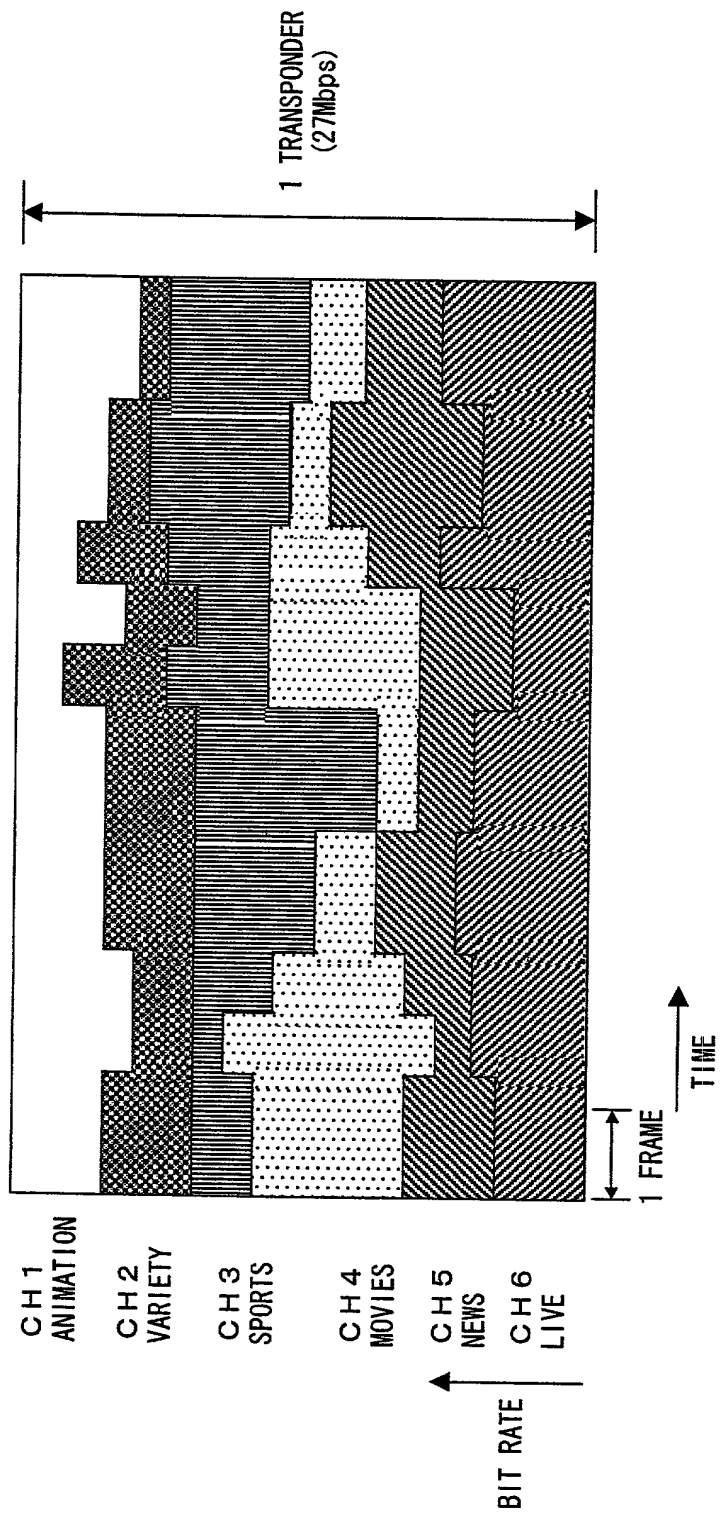


FIG. 3

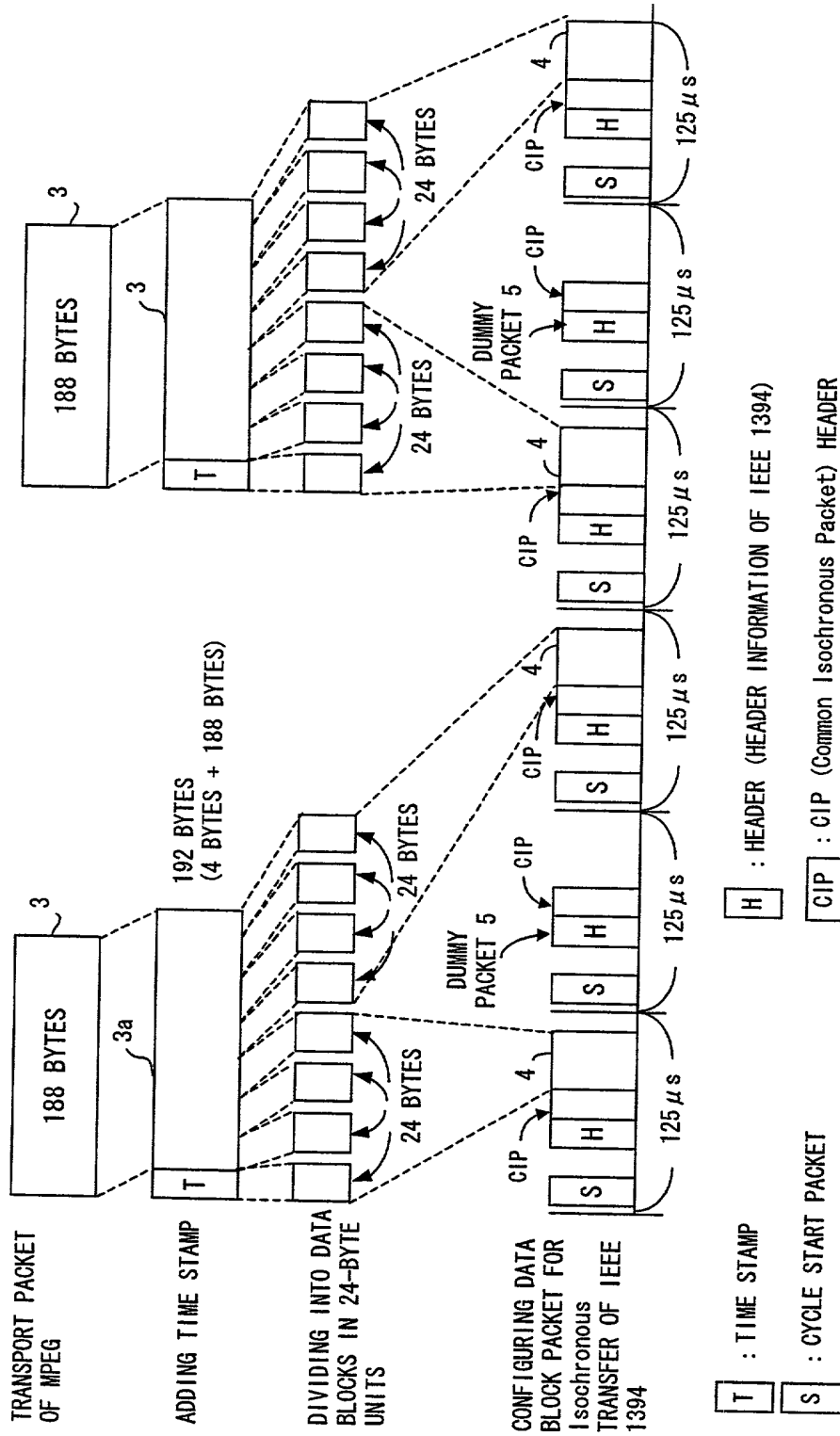
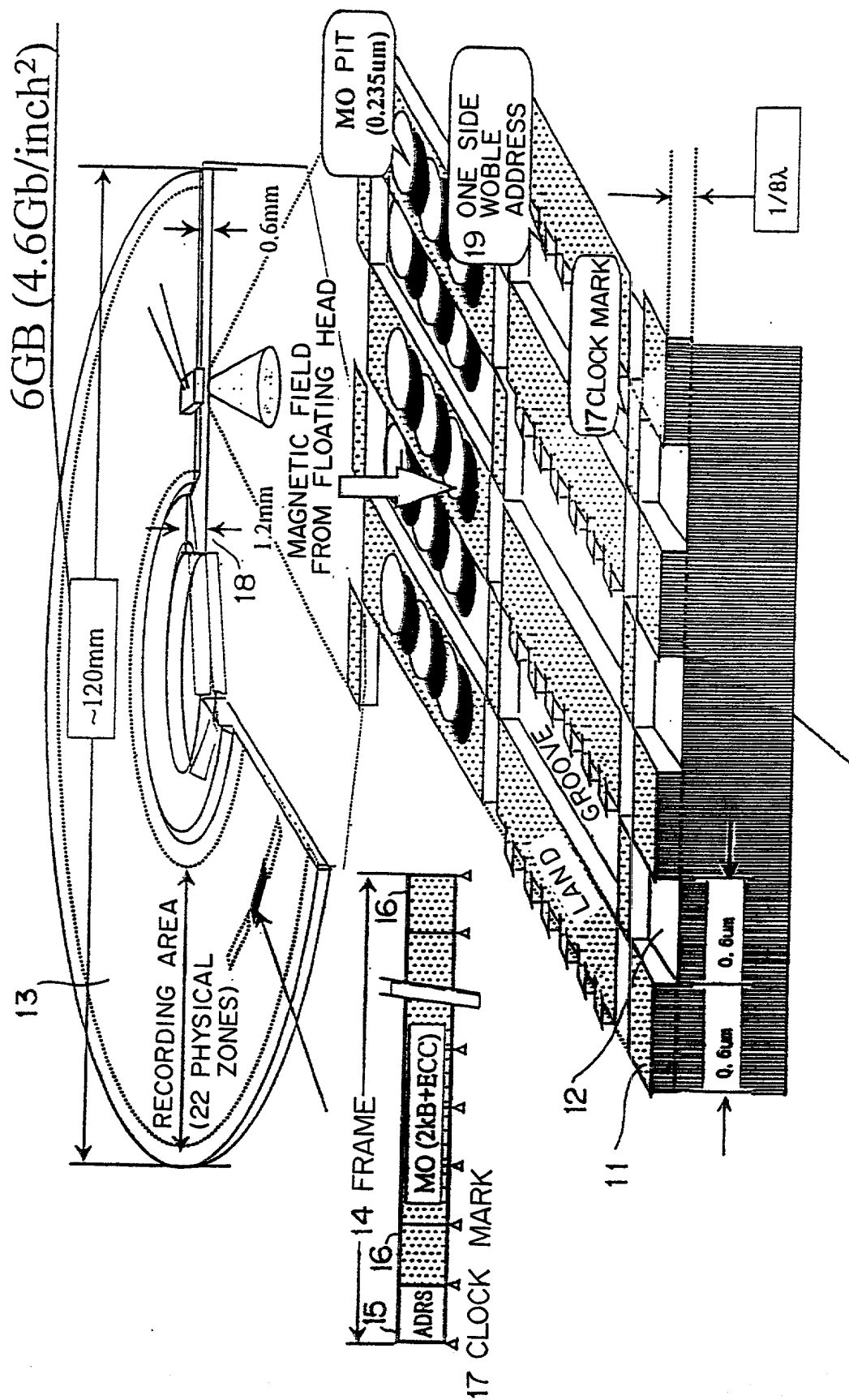


FIG. 4



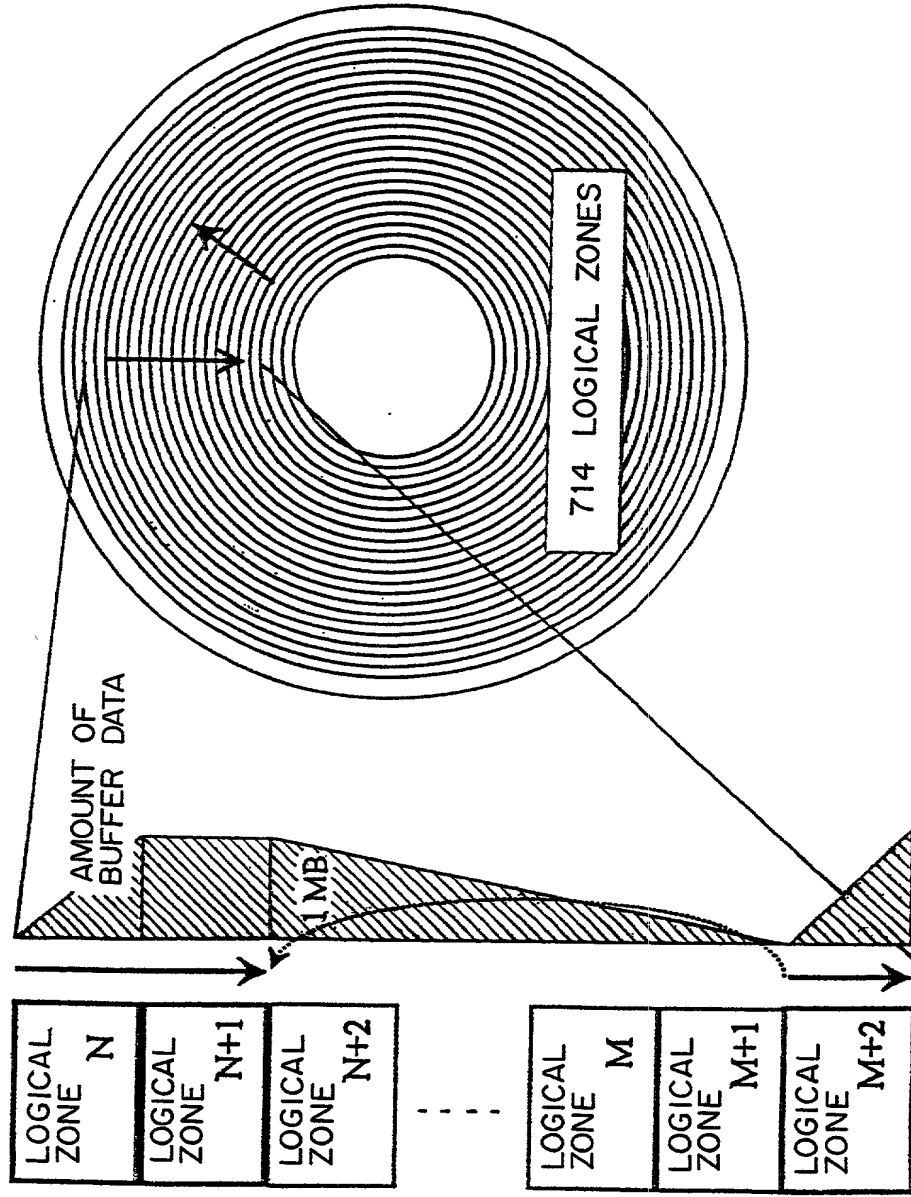


FIG. 6

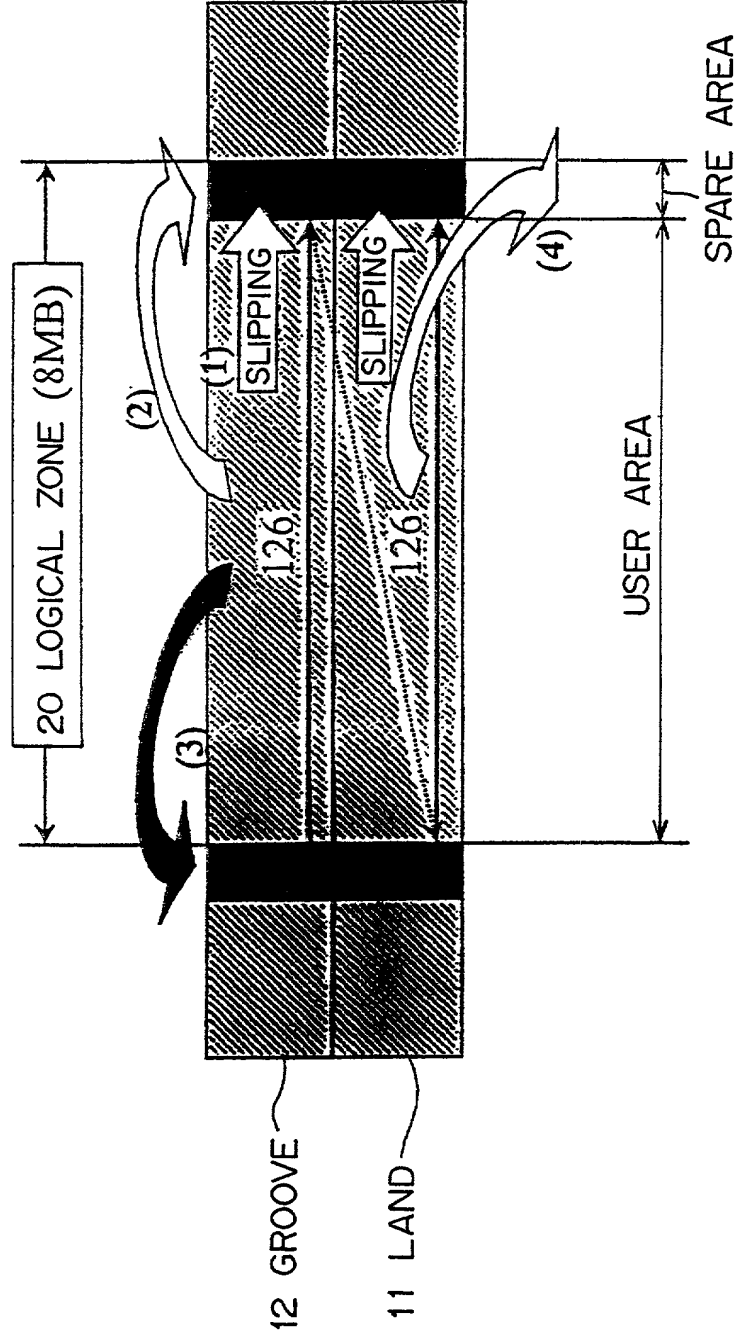


FIG. 7

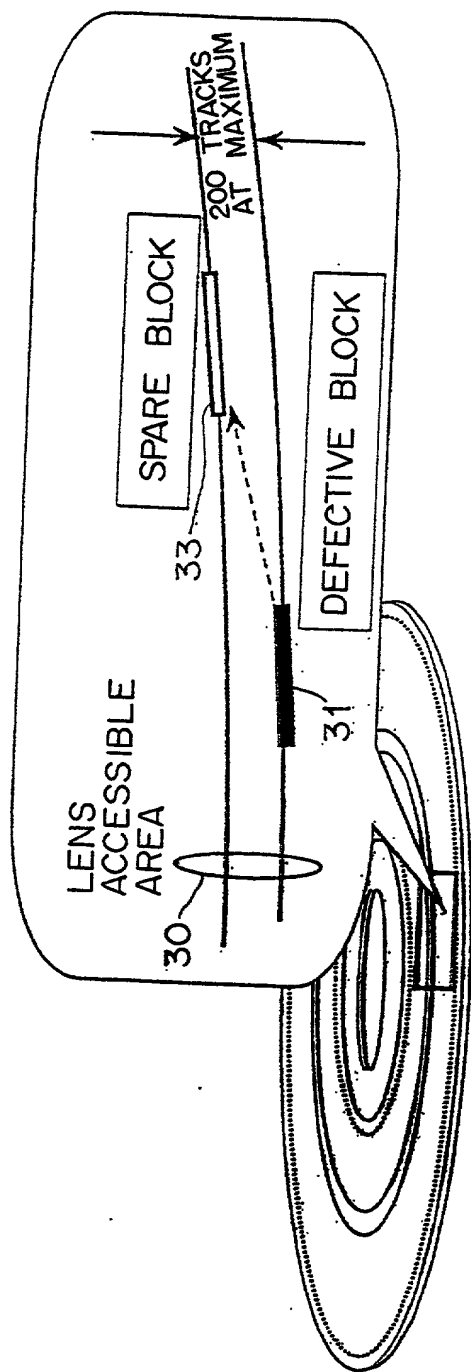


FIG. 8

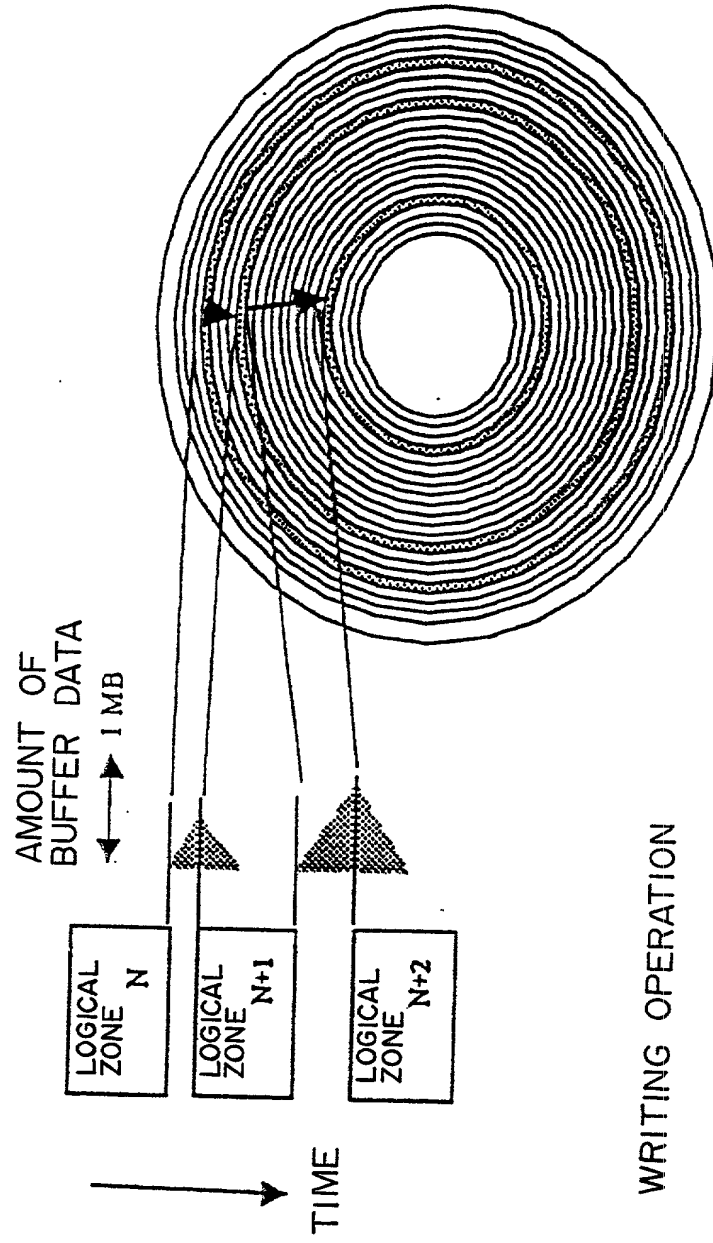


FIG. 9

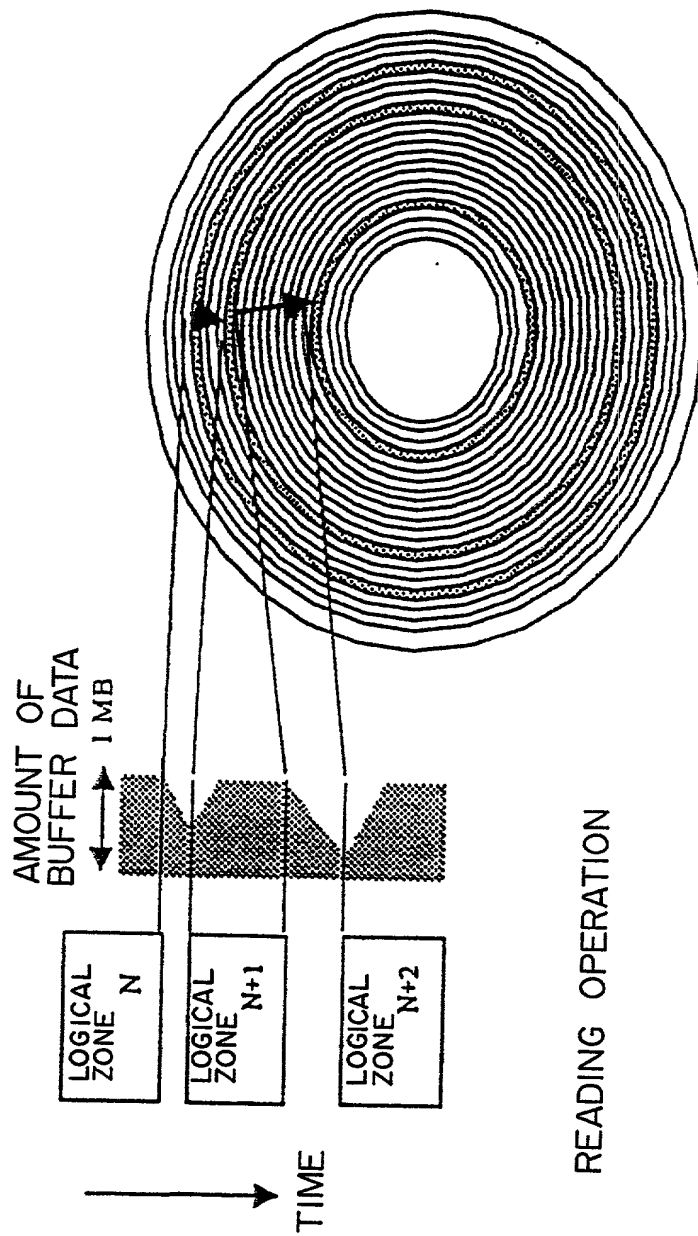


FIG. 10

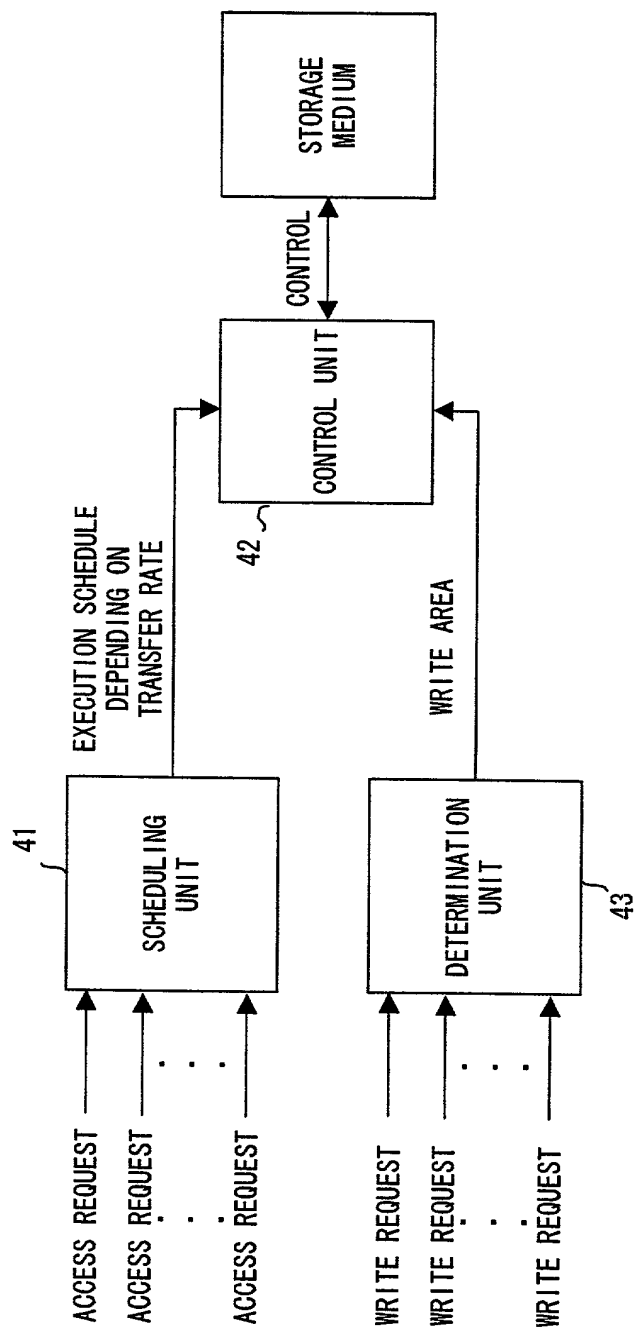


FIG. 11

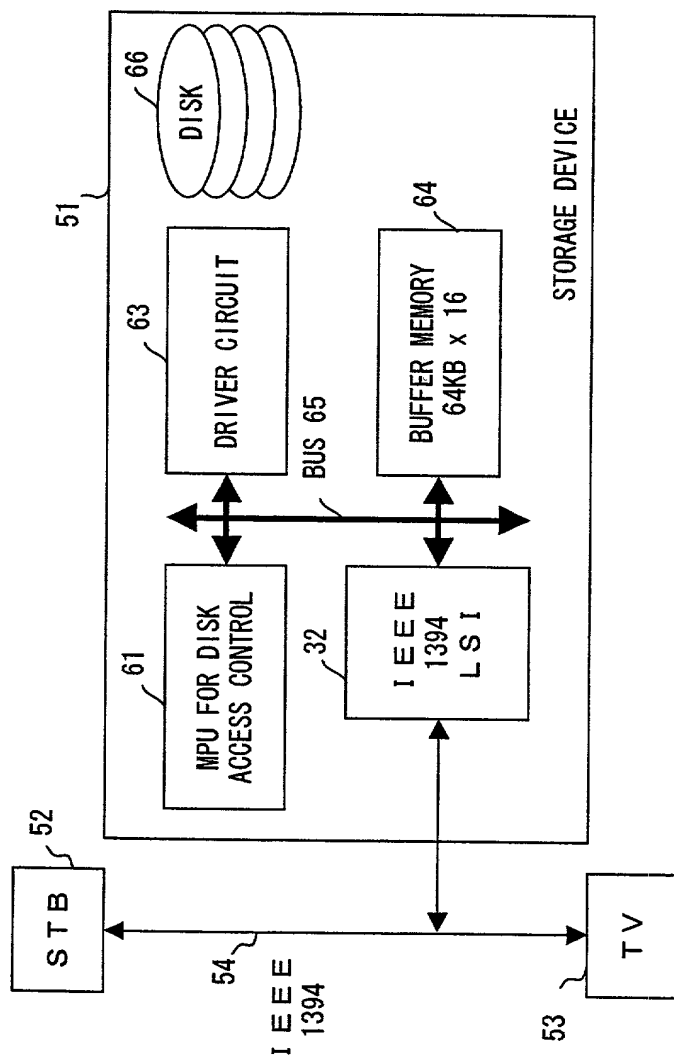


FIG. 12

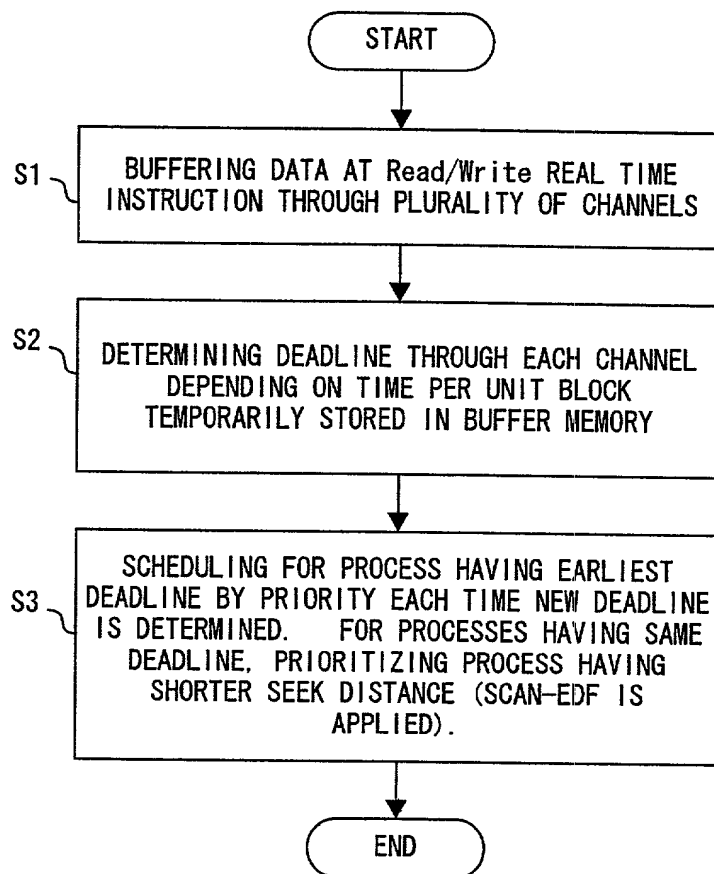


FIG. 13

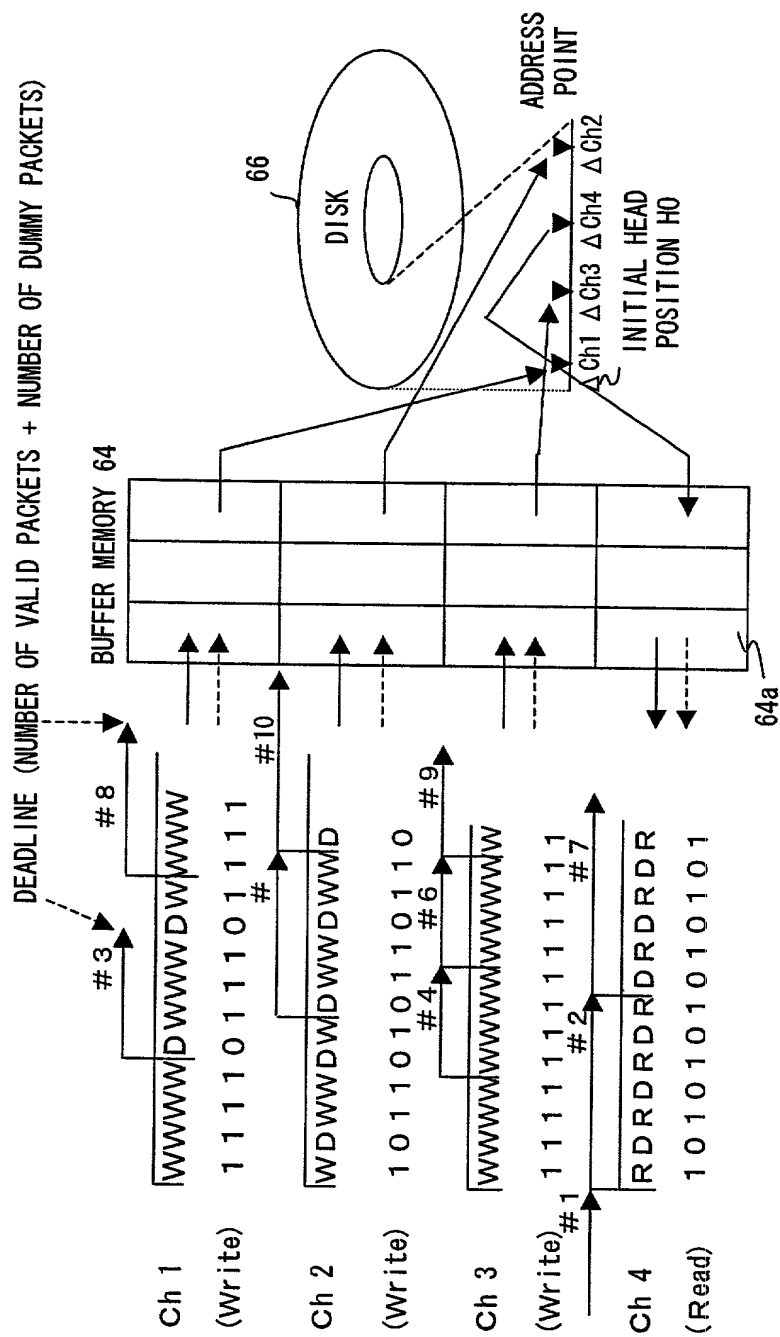


FIG. 14

FIG. 15

MAXIMUM TRANSFER RATE (NUMBER OF BYTES/ NUMBER OF PACKETS)	DEADLINE INFORMATION	BINARY DATA	VALID DATA
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FIG. 15

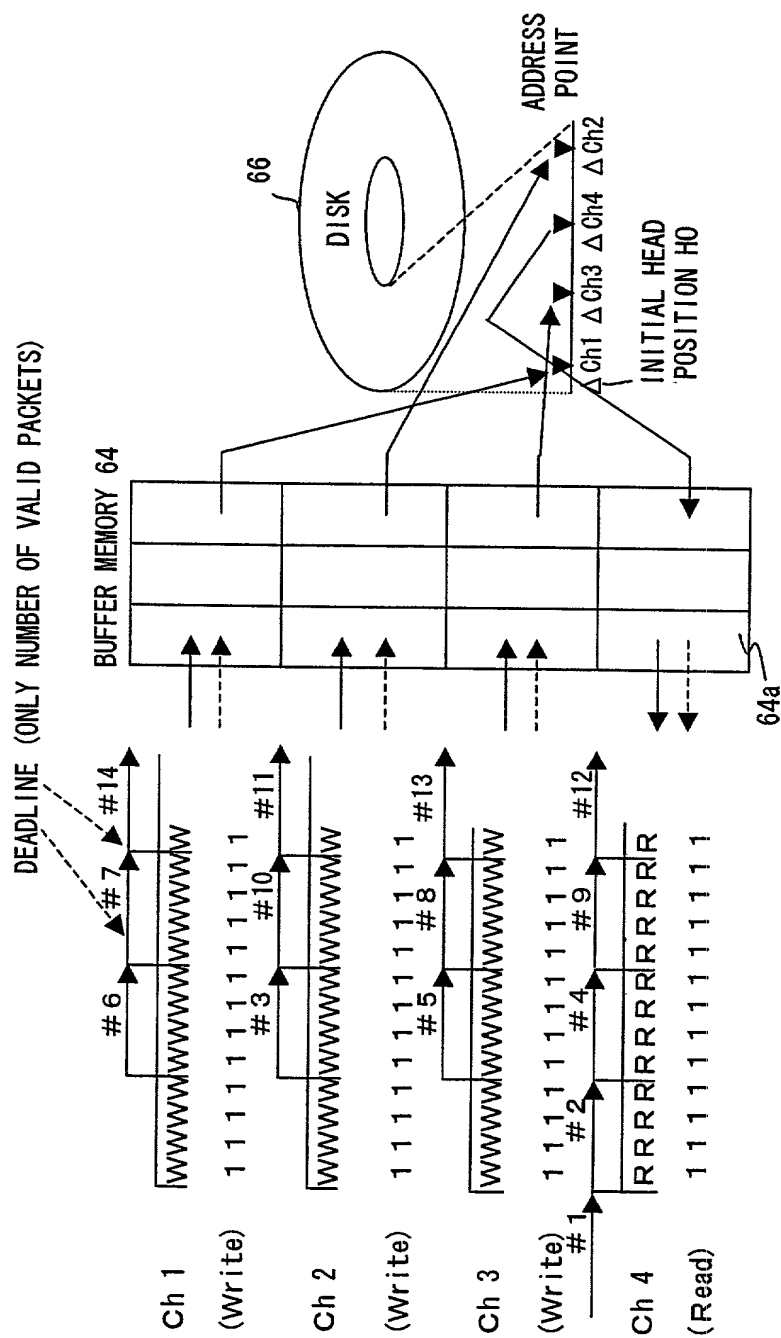


FIG. 16

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~

PROCESS ORDER	DEADLINE T	R/W	CHANNEL C	BLOCK ADDRESS A ON DISK
1	T _i	W _i	C _i	A _i
⋮	⋮	⋮	⋮	⋮
⋮	⋮	⋮	⋮	⋮
m-1	T _j	R _j	C _j	A _j
m	T _k	R _k	C _k	A _k
⋮	⋮	⋮	⋮	⋮
⋮	⋮	⋮	⋮	⋮
2N				

REGISTRATION →

Order (1) = {T, R/W, C, A}

FIG. 17

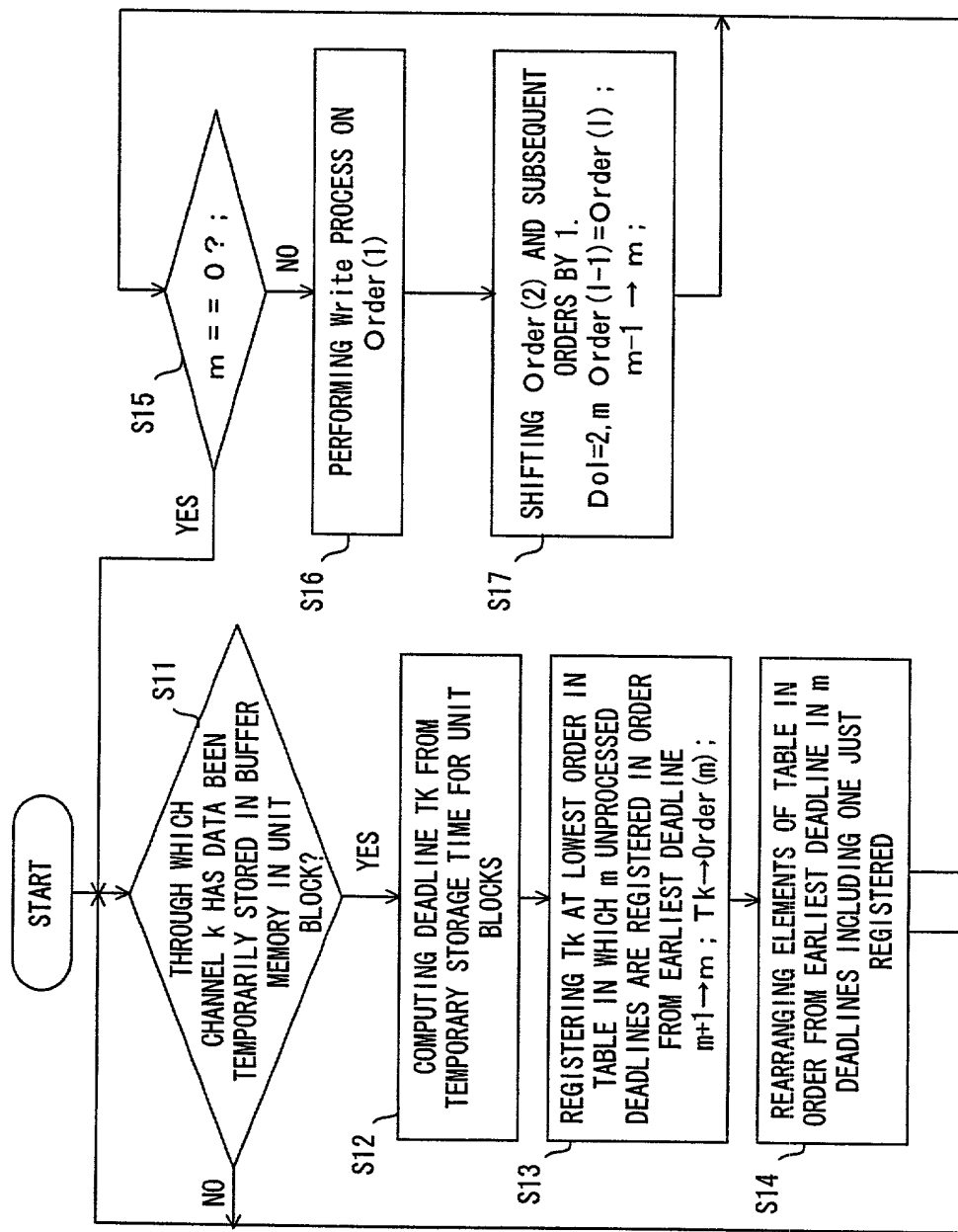


FIG. 18

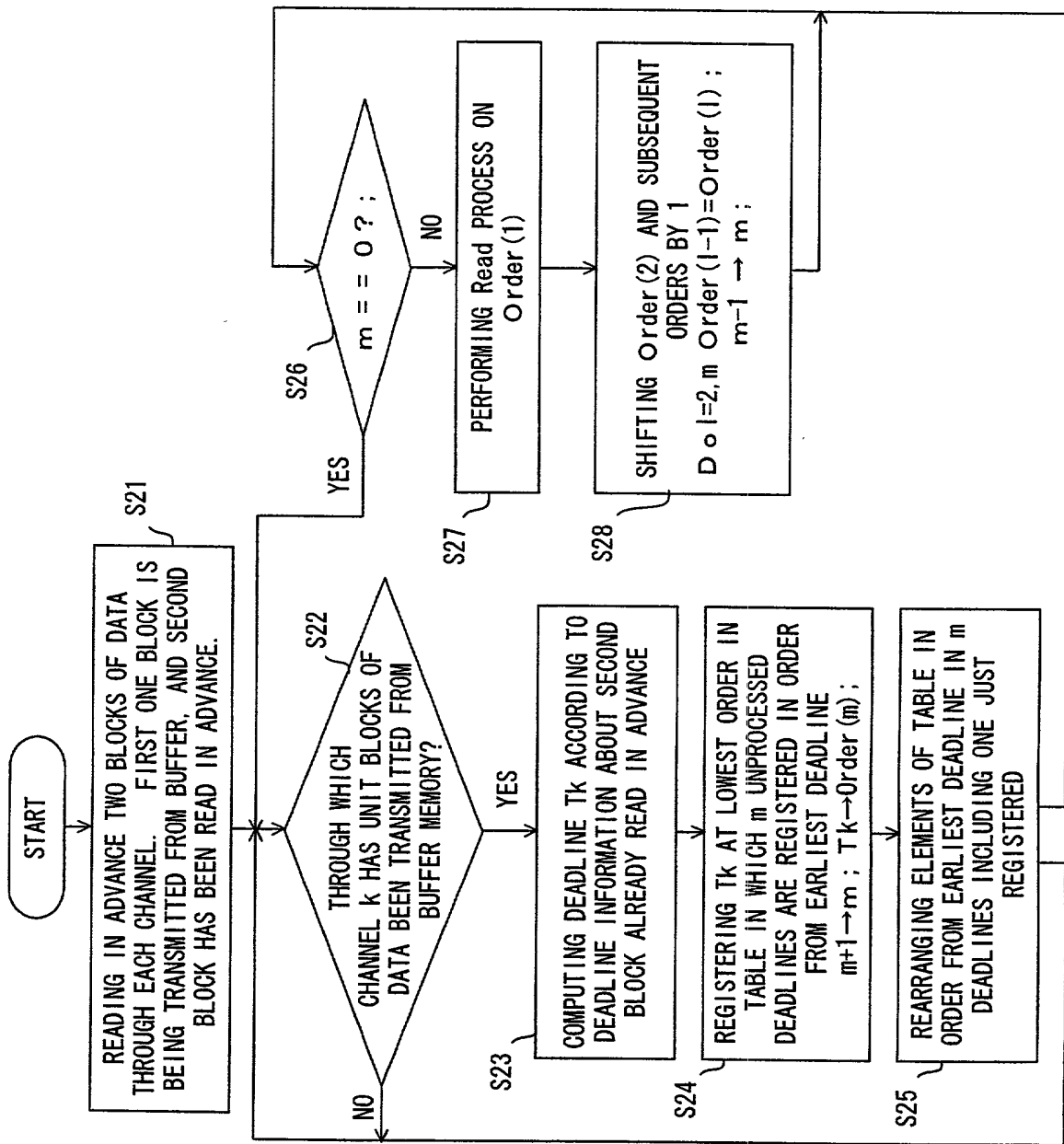


FIG. 19

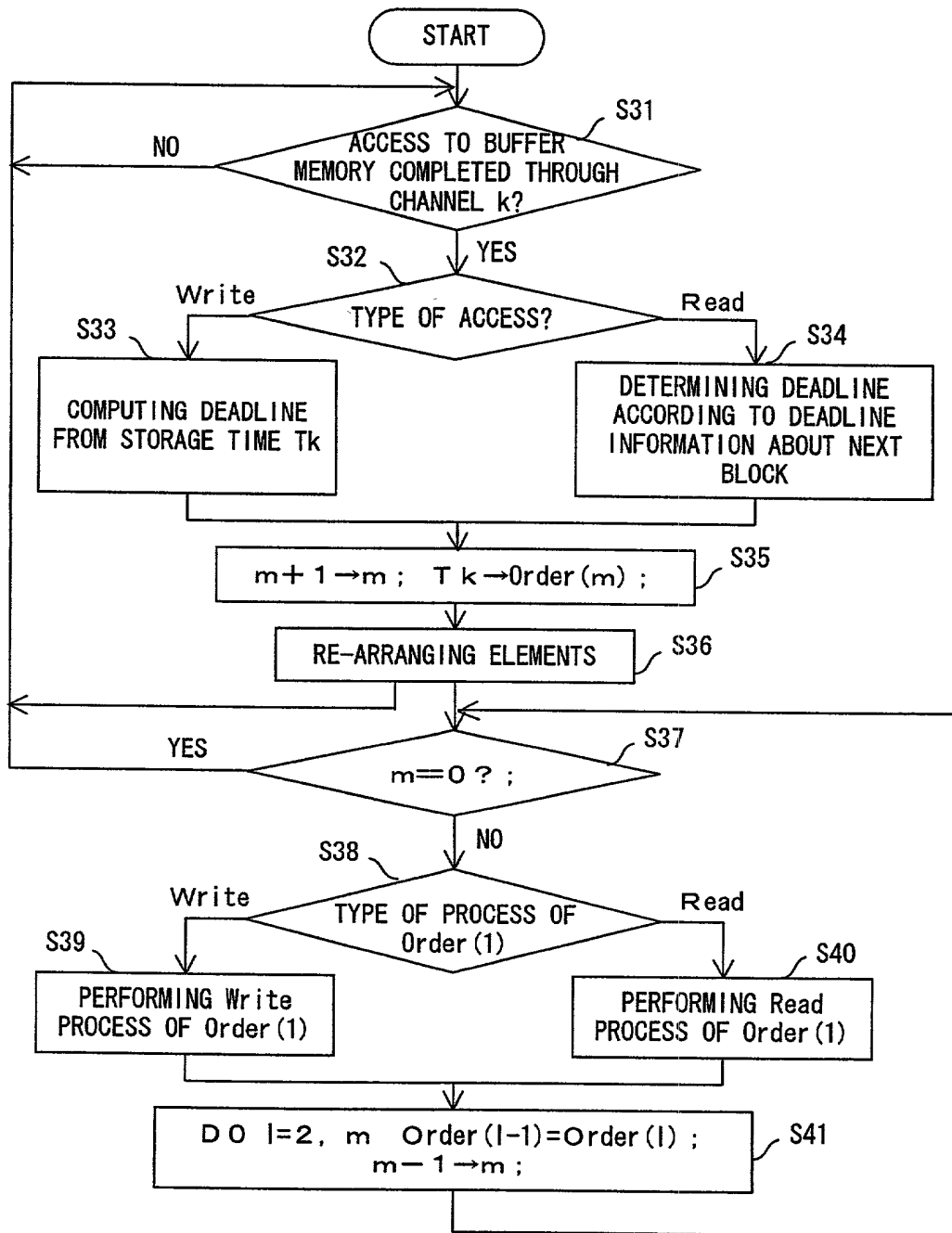


FIG. 20

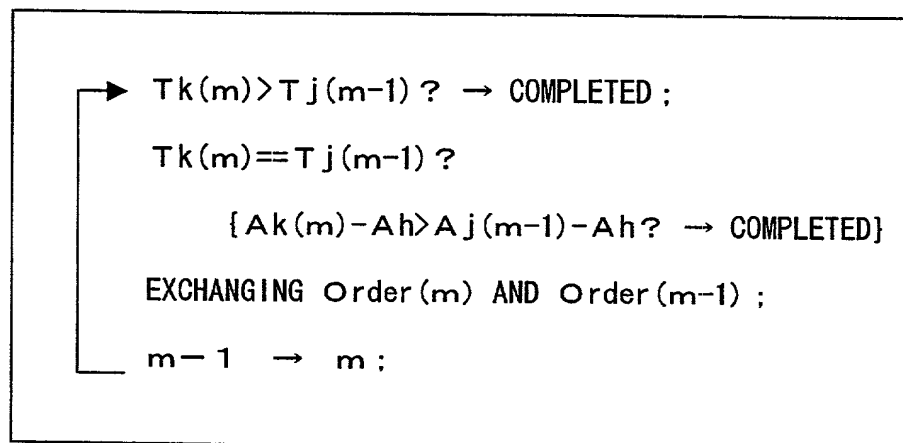


FIG. 21

$\langle T_j(3m/4) \rangle$?

$$T_k(m) < T_j(m/2) ? \rightarrow > T_j(m/4) ?$$

$\langle T_j(m/4) \rangle$?

INSERTING Order (m) IN ORDER OF DETERMINATION ;

FIG. 22

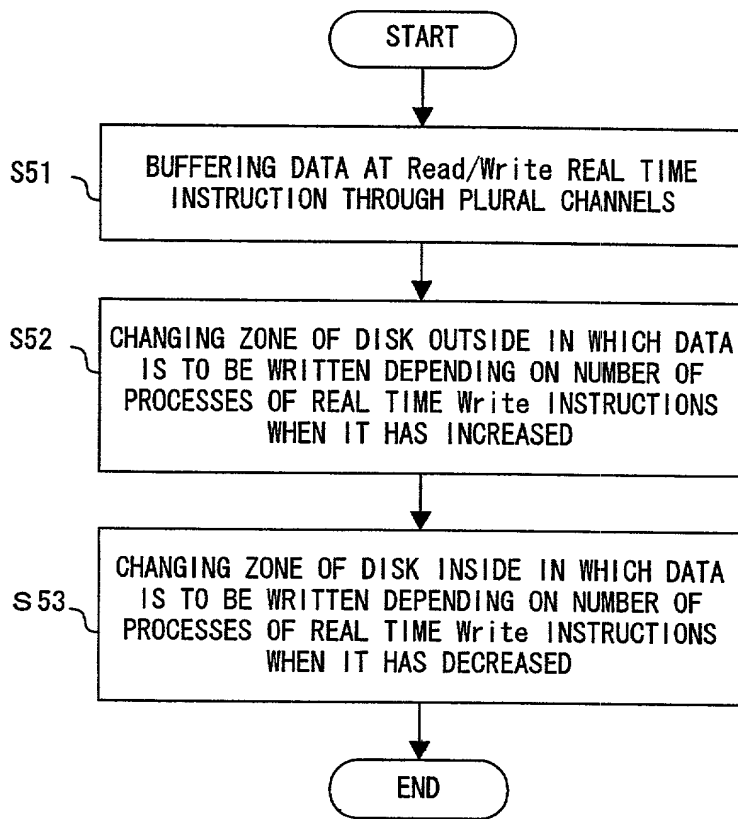


FIG. 23

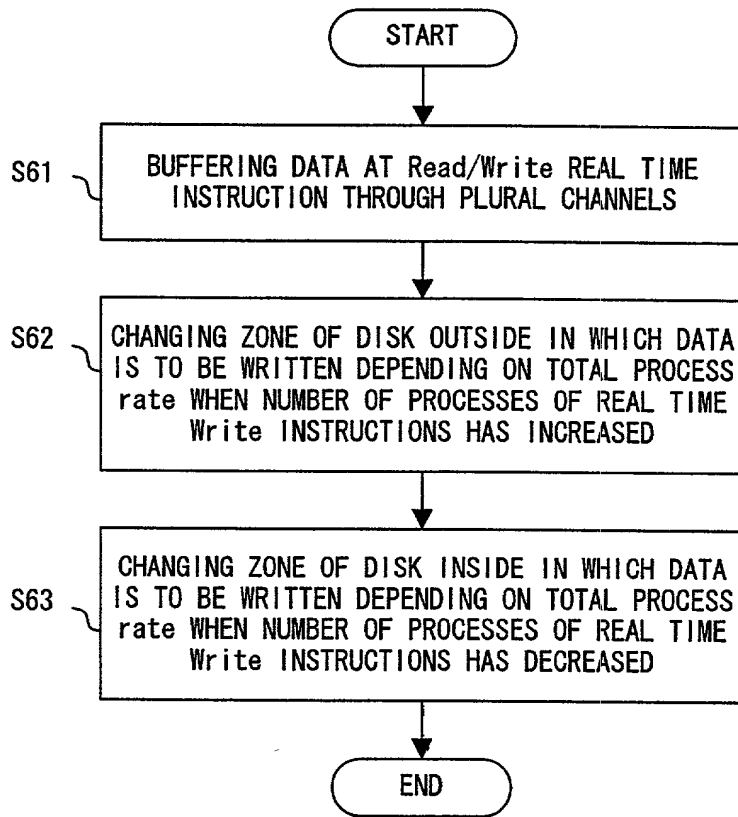


FIG. 24

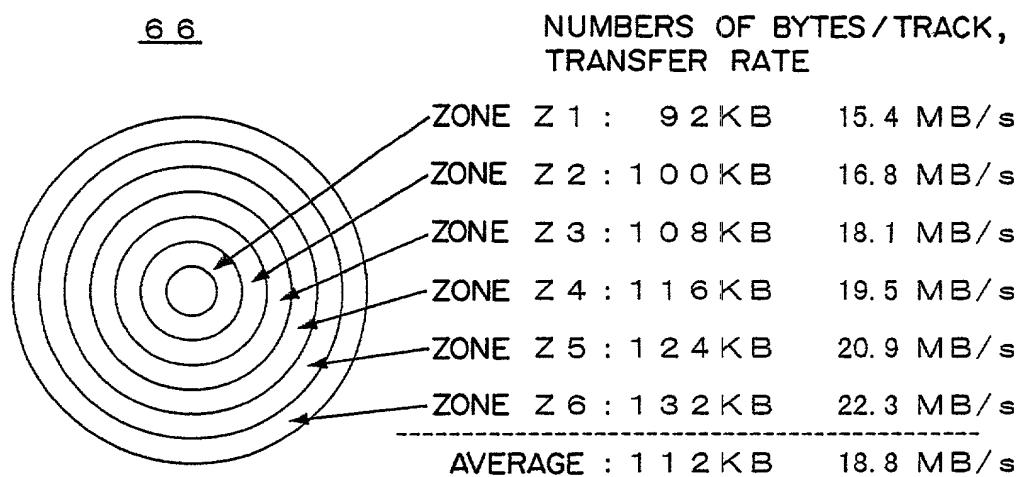


FIG. 25

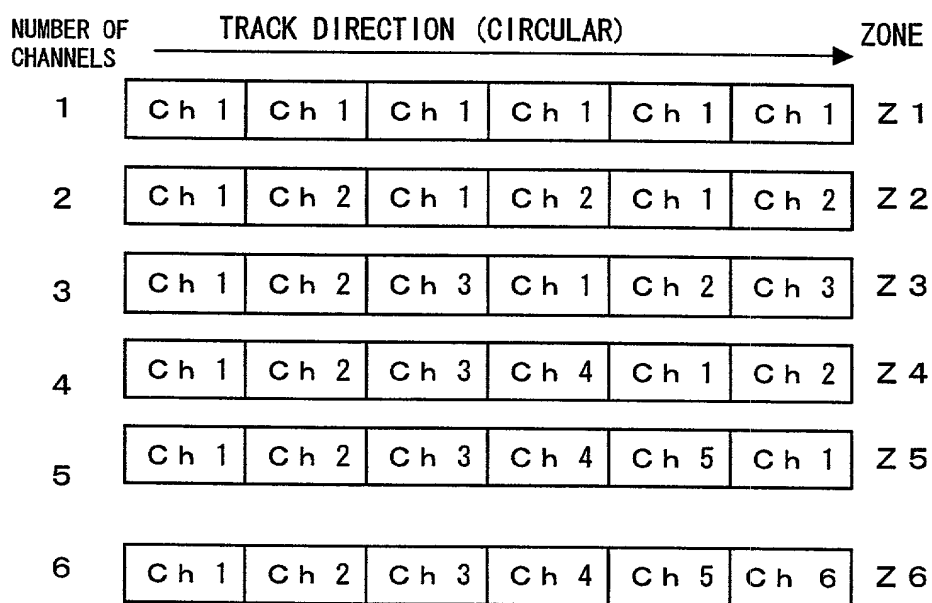


FIG. 26

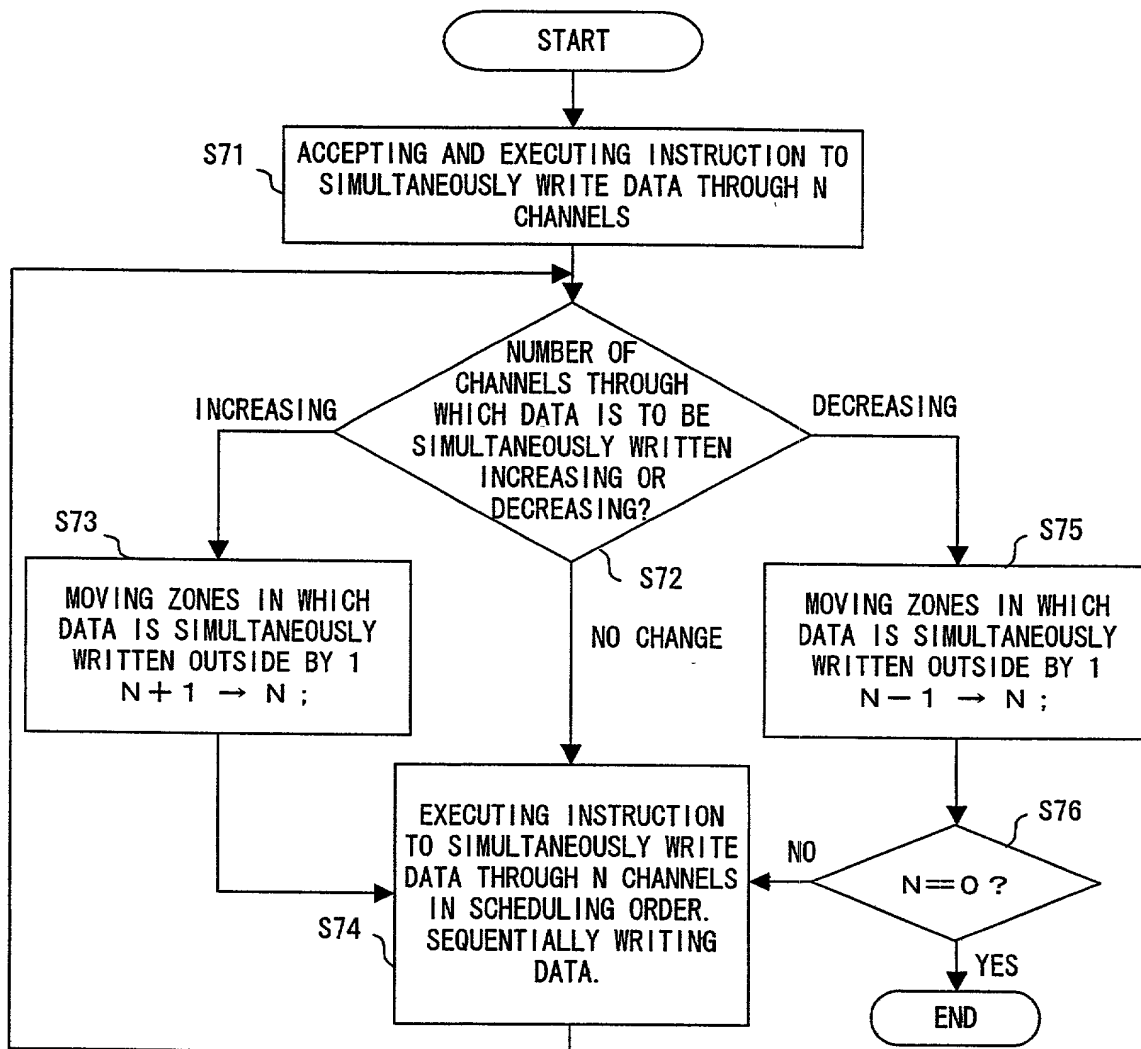


FIG. 27

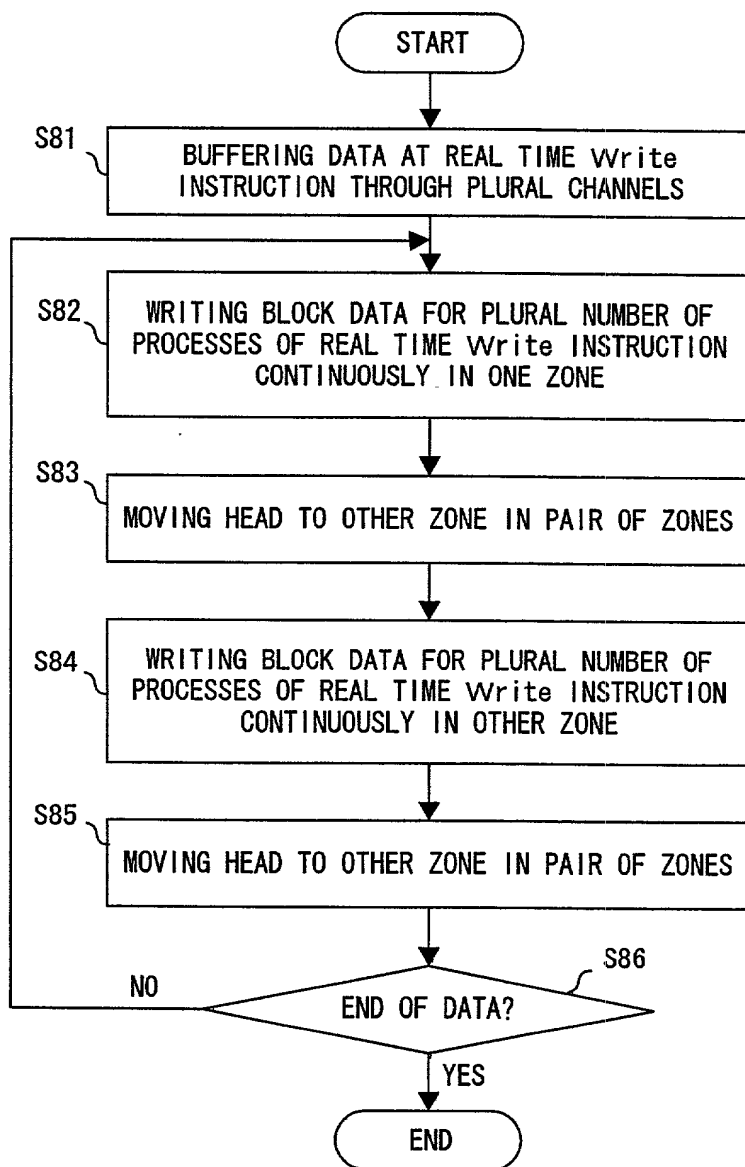
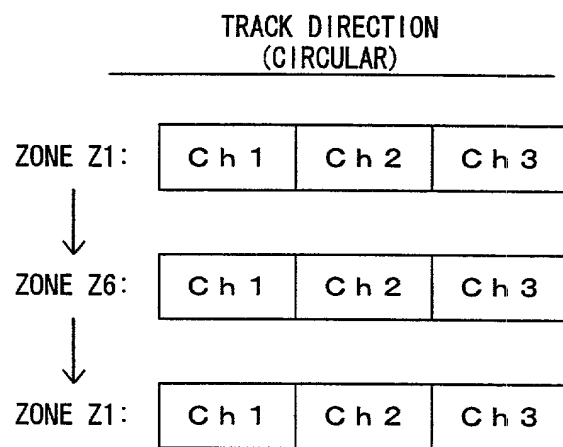


FIG. 28



F I G. 29

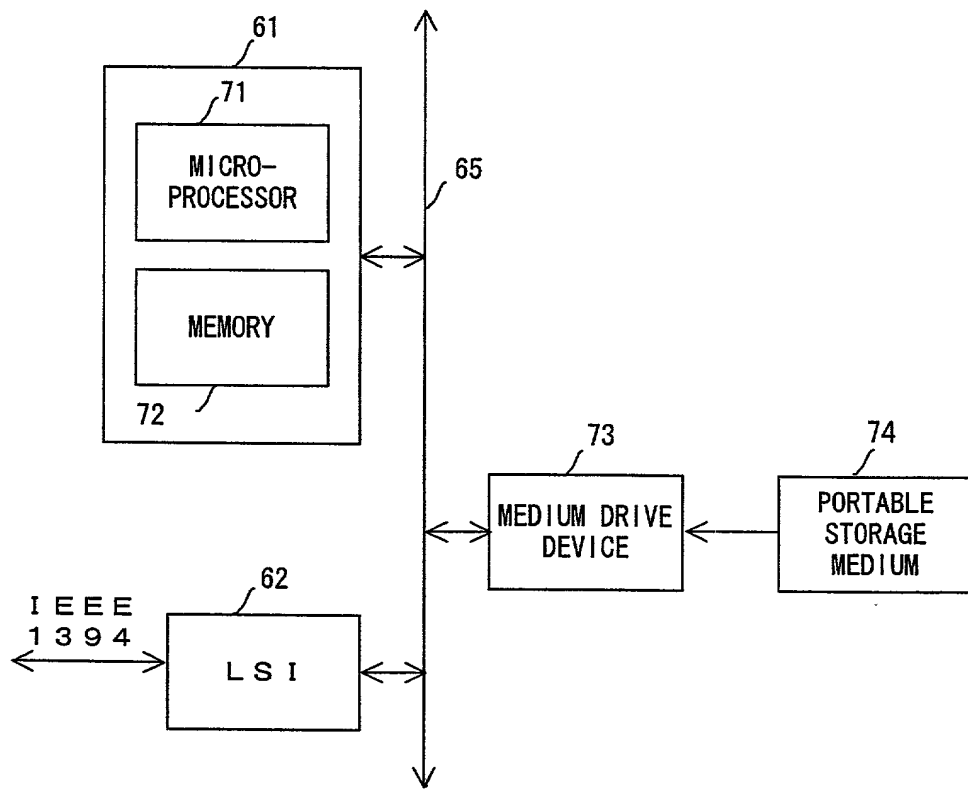


FIG. 30

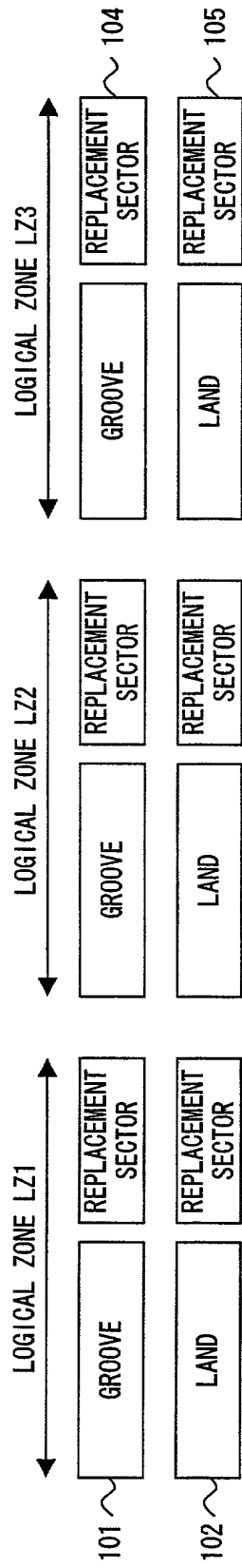


FIG. 31

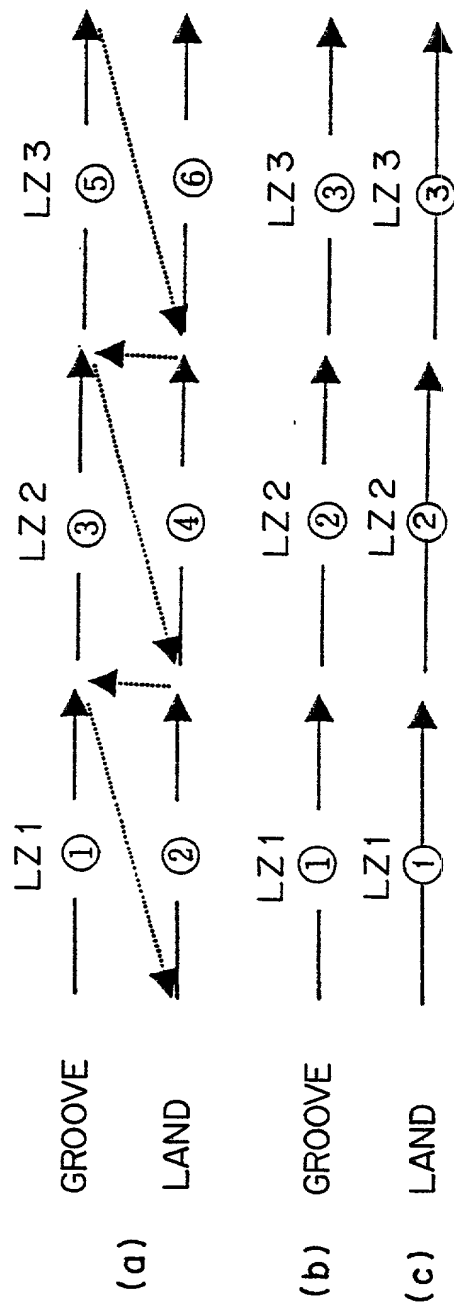


FIG. 32

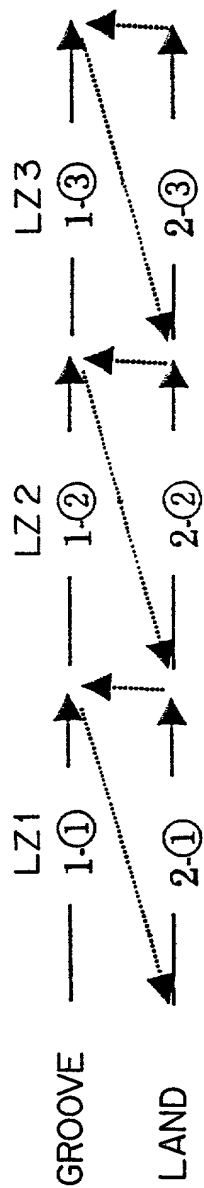


FIG. 33

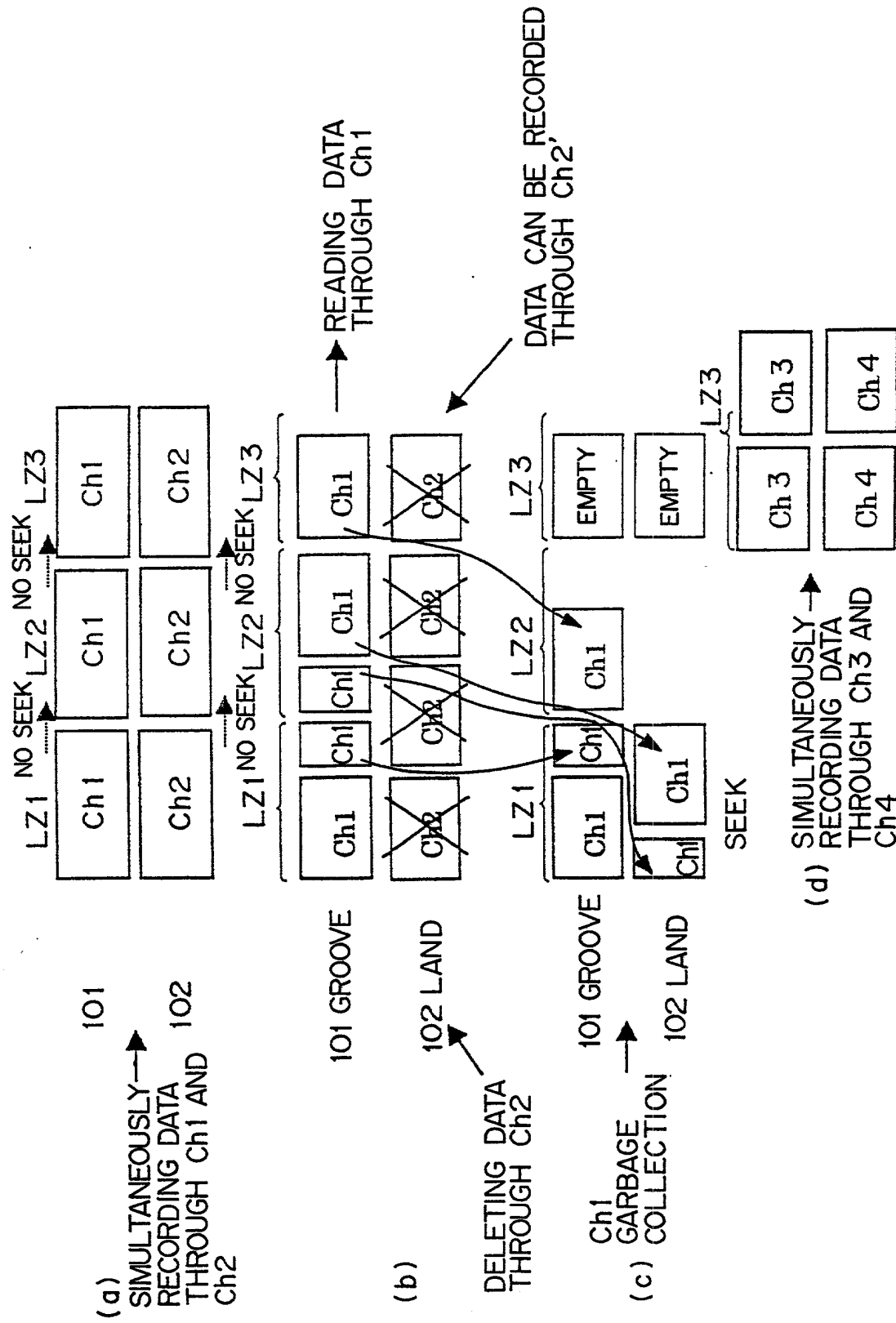


FIG. 34

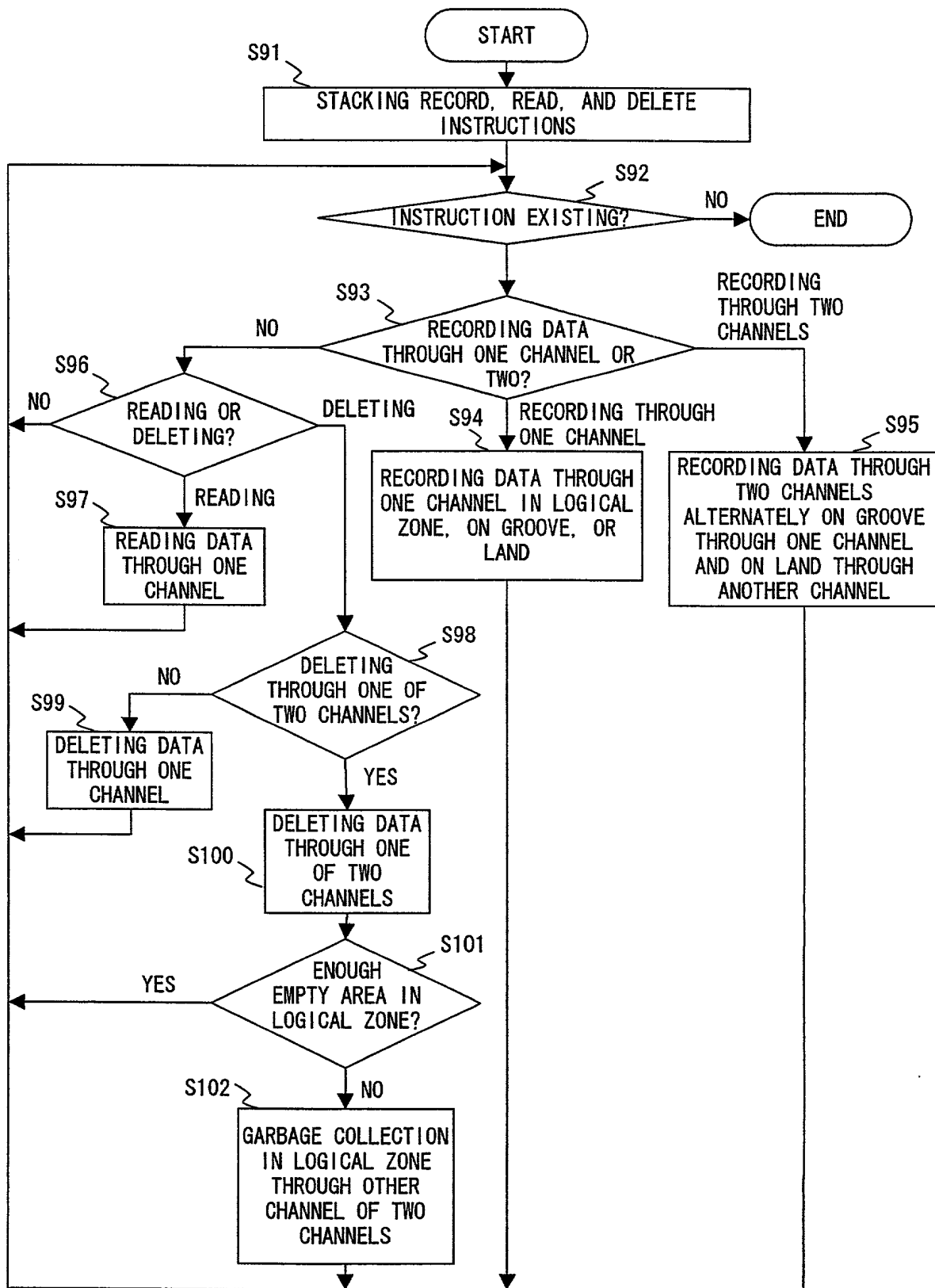


FIG. 35

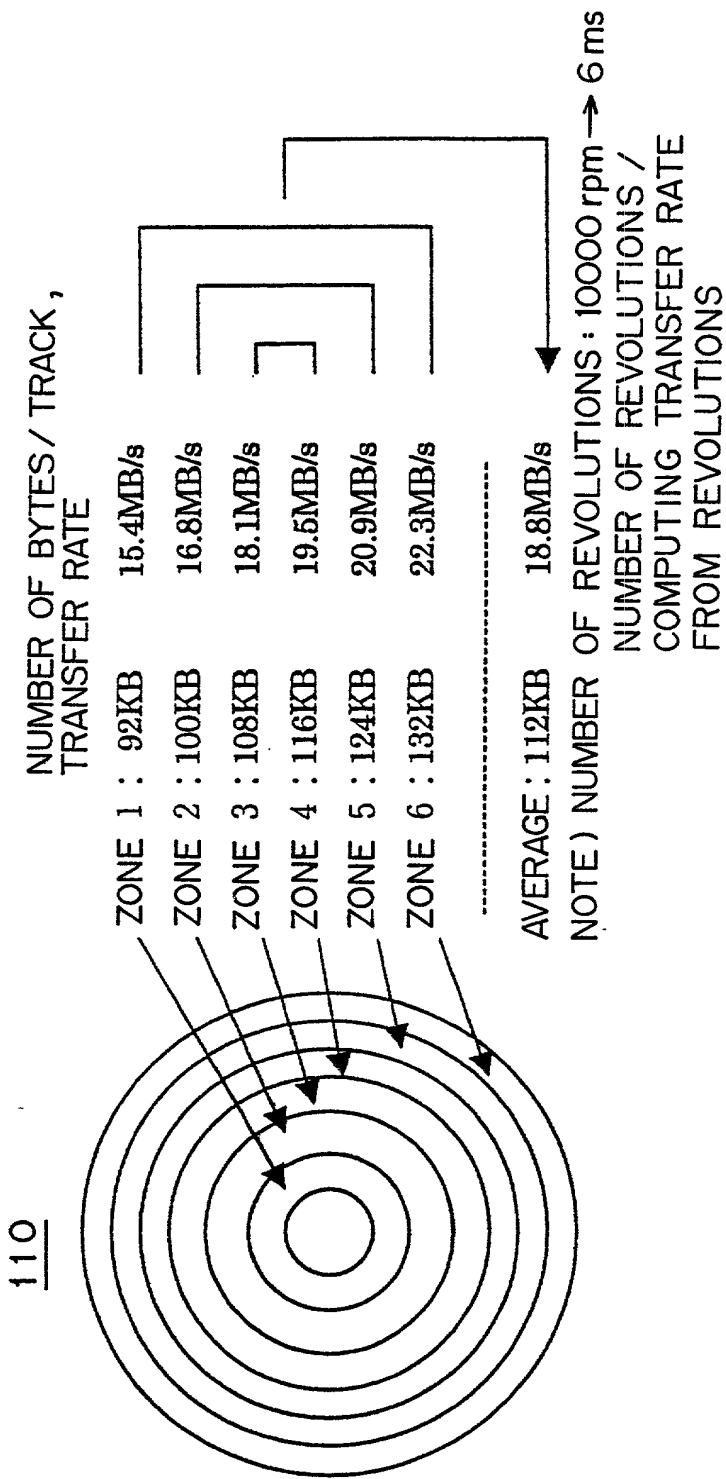


FIG. 36

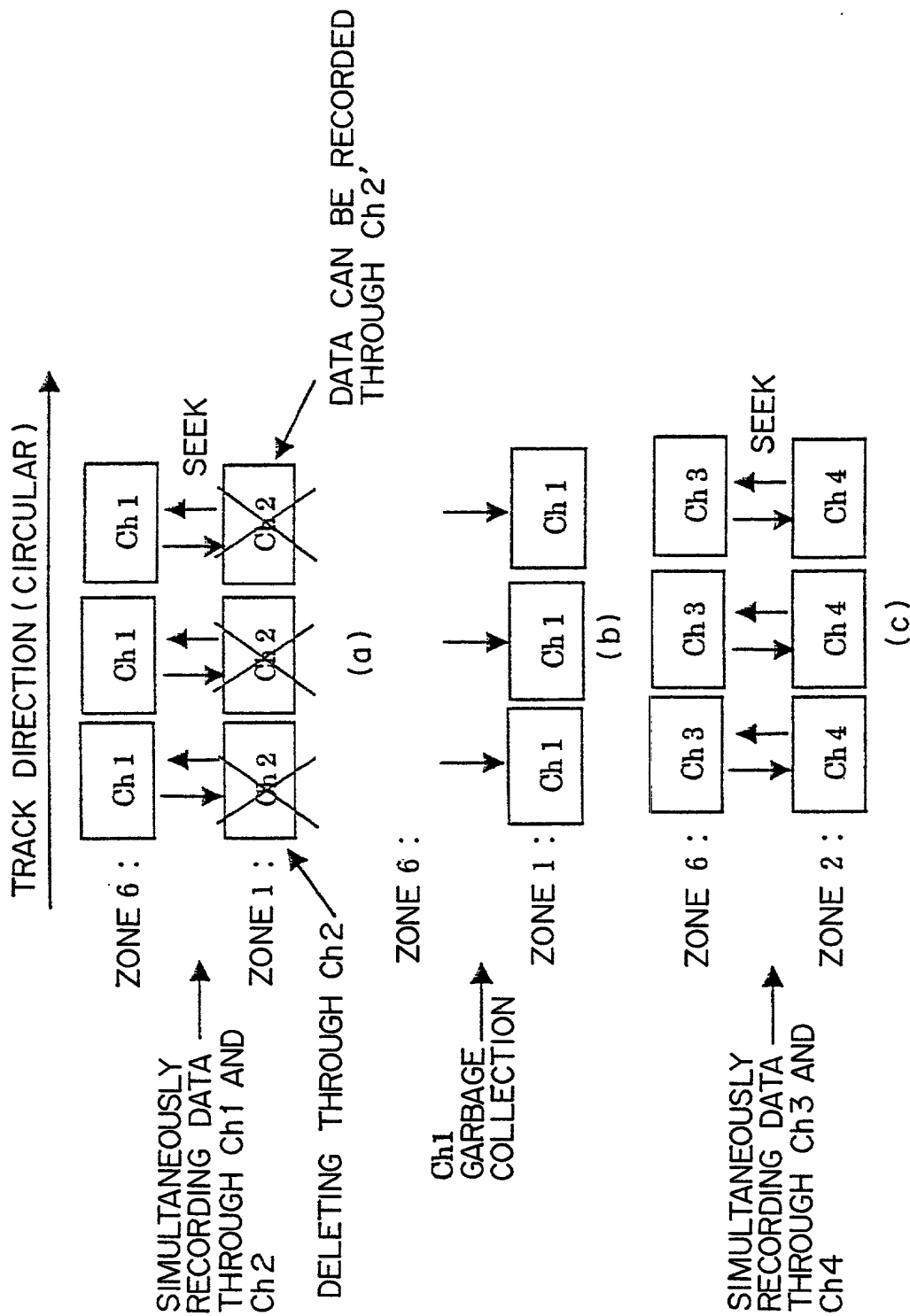


FIG. 37

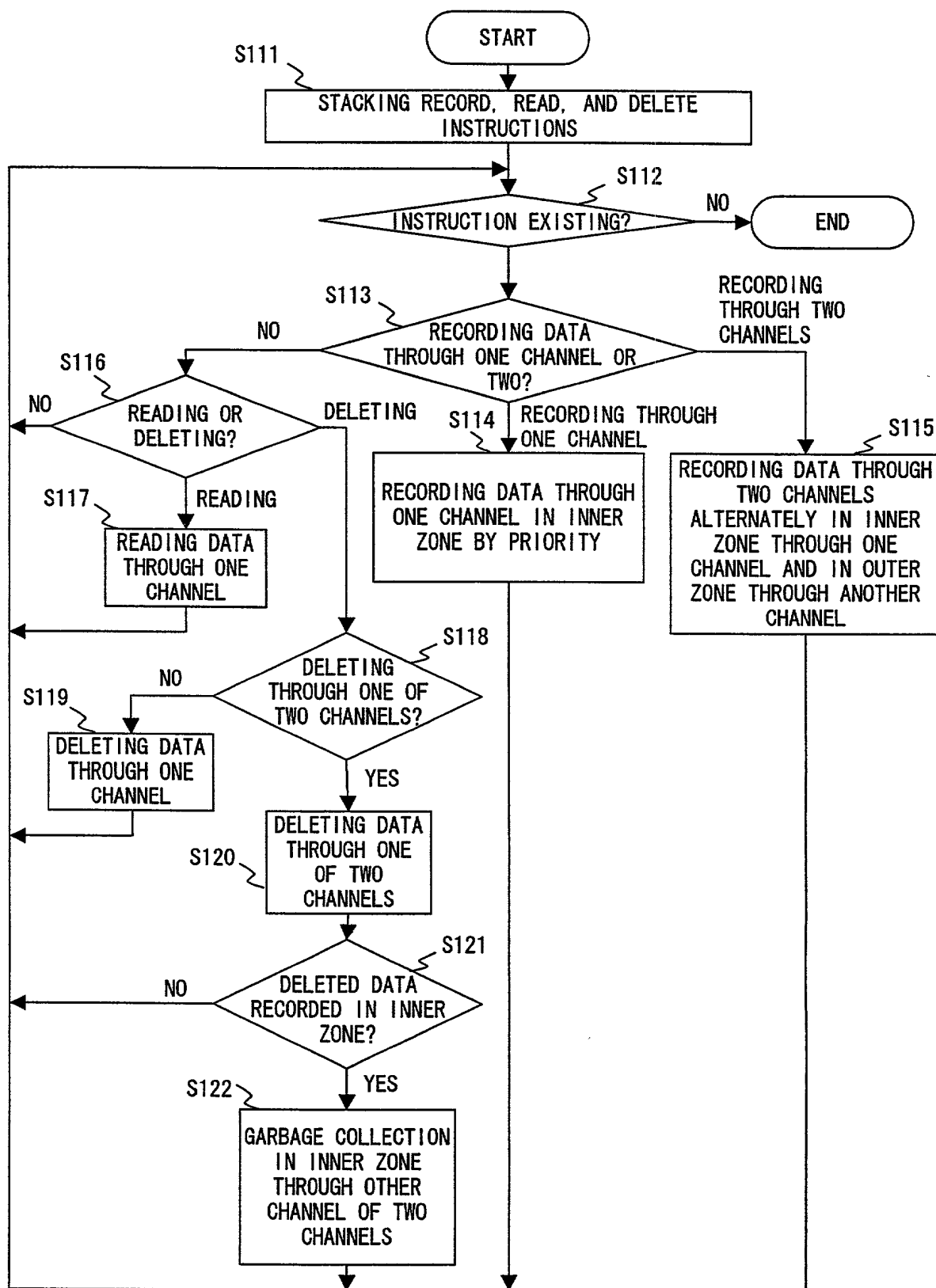


FIG. 38

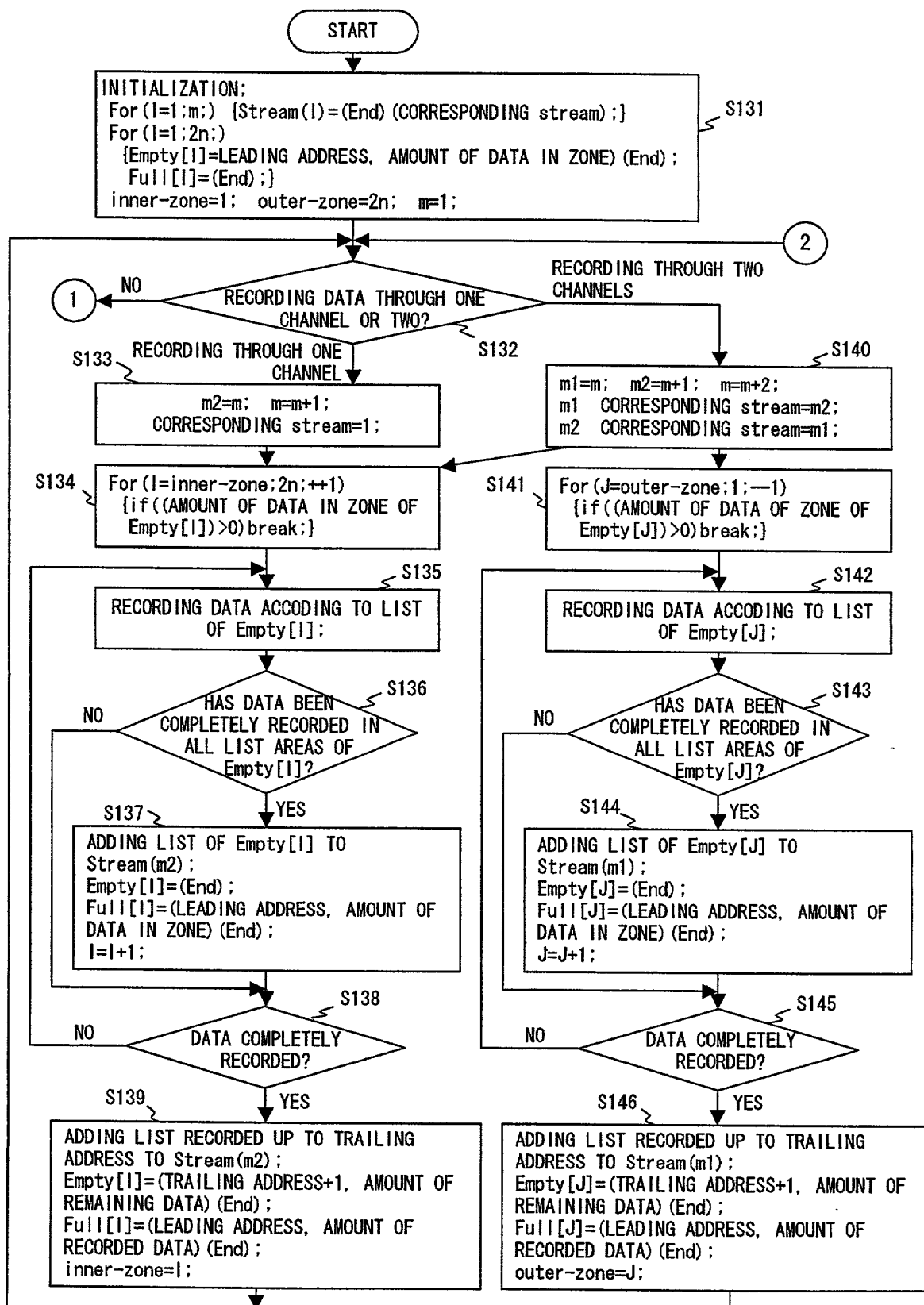


FIG. 39

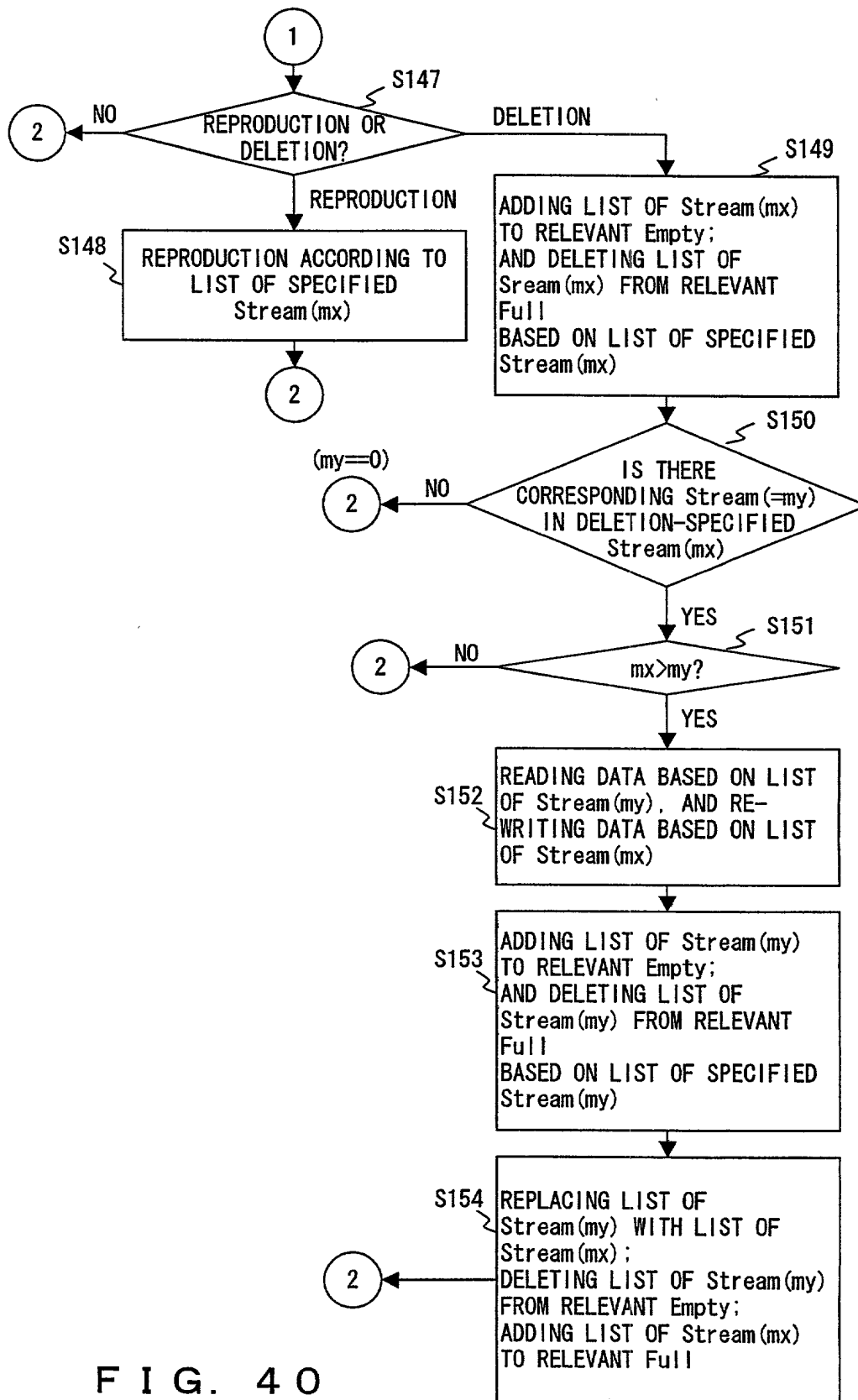


FIG. 40

STREAM LIST	(LEADING ADDRESS, AMOUNT OF DATA) → COMPLETION, CORRESPONDING stream FOR RECORDING DATA THROUGH TWO CHANNELS
Stream(1)	(Add, Data) → (Add, Data) →.....→ End.m?
· · ·	· · ·
Stream(m)	(Add, Data) → (Add, Data) →.....→ End.m?

FIG. 41

INNER/ OUTER	ZONE	EMPTY/FULL LIST	(LEADING ADDRESS, AMOUNT OF DATA) → COMPLETION : LIST STRUCTURE
INNER ZONE	1	Empty[1]	(Add, Data) → (Add, Data) →.....→ End
		Full[1]	(Add, Data) → (Add, Data) →.....→ End
	n	Empty[n]	(Add, Data) → End (INITIAL VALUE)
		Full[n]	End (INITIAL VALUE)
OUTER ZONE	n+1	Empty[n+1]	(Add, Data) → End (INITIAL VALUE)
		Full[n+1]	End (INITIAL VALUE)
	2n	Empty[2n]	(Add, Data) → (Add, Data) →.....→ End
		Full[2n]	(Add, Data) → (Add, Data) →.....→ End

FIG. 42

ZONE no.	NUMBER OF BYTES/TRACK	NUMBER OF TRACKS	NUMBER OF SECTORS	SECTOR ADDRESS
1	92KB	1000	184k	1-184000
2	100KB	1000	200k	184000-384000
3	108KB	1000	216k	384001-600000
4	116KB	1000	232k	600001-832000
5	124KB	1000	248k	832001-1080000
6	132KB	1000	264k	1080001-1344000

*: 512B/SECTOR

F I G. 4 3

	STORAGE ADDRESS	MEANING OF INFORMATION	STORED DATA (LEADING STORAGE DEVICE, AMOUNT OF DATA, NEXT STORAGE ADDRESS)
INITIALIZED AREA	0	End	(0, 0, 0)
	1	Stream(1)	① (0, 0, 0) ③ → (1080001, 264000, 19) ⑤ → (1, 184000, 20)
	2	Stream(2)	① (0, 0, 0) ② → (1, 184000, 18) ④ → (0, 0, 0)
	3	Stream(3)	① (0, 0, 0) ⑦ → (1080001, 256000, 22)
	4	Stream(4)	① (0, 0, 0) ⑥ → (300001, 84000, 21)
	5	Stream(5)	① (0, 0, 0)
	6	Empty[1]	① (1, 184000, 0) ② → (0, 0, 0) ④ → (1, 184000, 0) ⑤ → (0, 0, 0)
	7	Empty[2]	① (184001, 200000, 0) ③ → (300001, 84000, 0) ④ → (184001, 200000, 0) ⑤ → (300001, 84000, 0) ⑥ → (0, 0, 0)
	8	Empty[3]	① (384001, 216000, 0) ⑦ → (0, 0, 0)
	9	Empty[4]	① (600001, 232000, 0)
	10	Empty[5]	① (832001, 248000, 0) ③ → (876001, 204000, 0) ⑤ → (832001, 248000, 0) ⑦ → (876001, 204000, 0)
	11	Empty[6]	① (1080001, 256000, 0) ③ → (0, 0, 0) ⑤ → (1080001, 256000, 0) ⑦ → (0, 0, 0)
	12	Full[1]	① (1, 0, 0) ② → (1, 184000, 0) ④ → (1, 0, 0) ⑤ → (1, 184000, 0)
	13	Full[2]	① (184001, 0, 0) ③ → (184001, 116000, 0) ④ → (184001, 0, 0) ⑤ → (184001, 116000, 0) ⑥ → (184001, 200000, 0)
	14	Full[3]	① (384001, 0, 0) ⑦ → (384001, 216000, 0)
	15	Full[4]	① (600001, 0, 0)
	16	Full[5]	① (832001, 0, 0) ③ → (832001, 44000, 0) ⑤ → (832001, 0, 0) ⑦ → (832001, 44000, 0)
	17	Full[6]	① (1080001, 0, 0) ③ → (1080001, 256000, 0) ⑥ → (1080001, 84001, 0) ⑦ → (1080001, 256000, 0)
EXTENSION AREA	18	$Z_1 \rightarrow Z_2$	(184001, 0, 0) ③ → (184001, 116000, 0)
	19	$Z_6 \rightarrow Z_5$	(832001, 0, 0) ③ → (832001, 44000, 0)
	20	$Z_1 \rightarrow Z_2$	(184001, 0, 0) ⑤ → (184001, 116000, 0)
	21	$Z_2 \rightarrow Z_3$	(384001, 0, 0) ⑦ → (384001, 216000, 0)
	22	$Z_6 \rightarrow Z_5$	(832001, 0, 0) ⑦ → (832001, 44000, 0)
	23		
	24		
	25		
	26		
	27		

FIG. 44

TOP SECRET

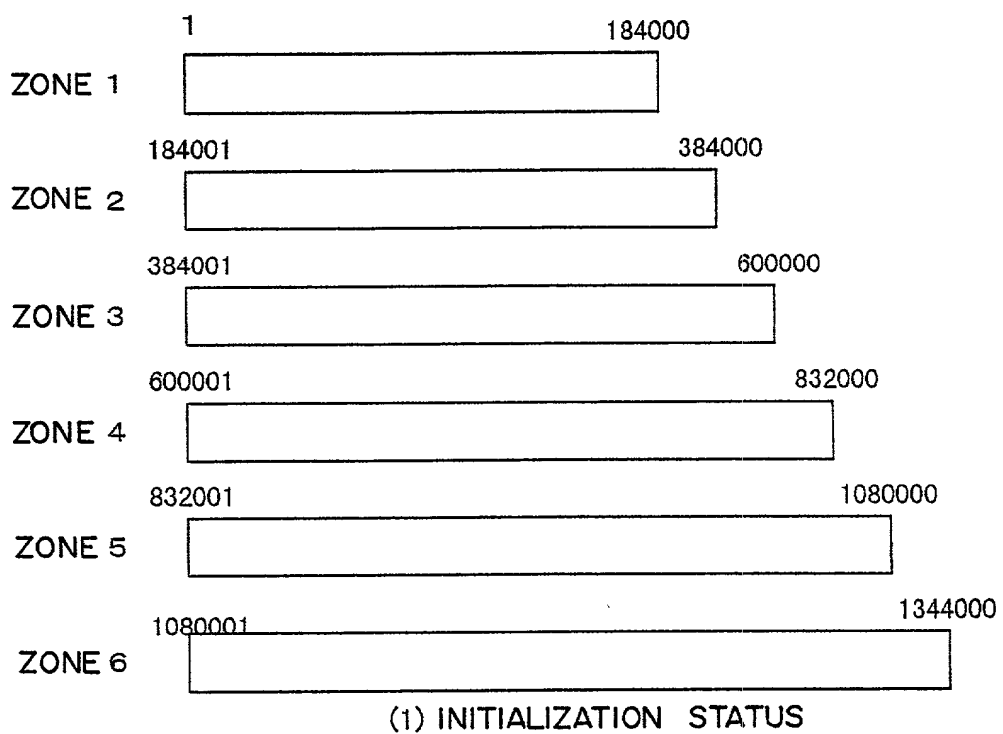


FIG. 45

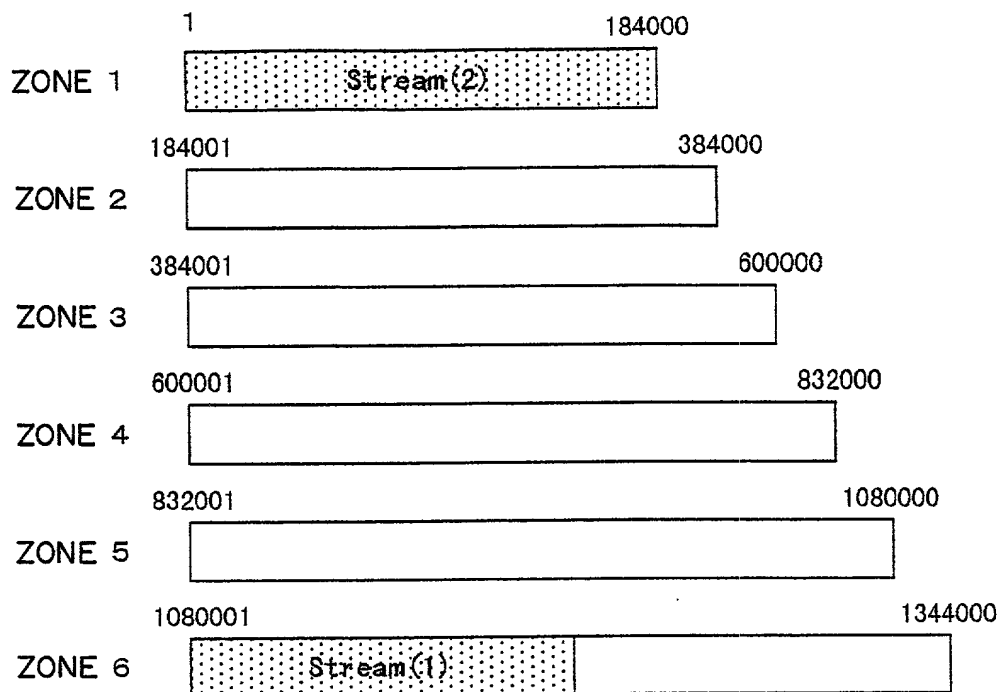


FIG. 46

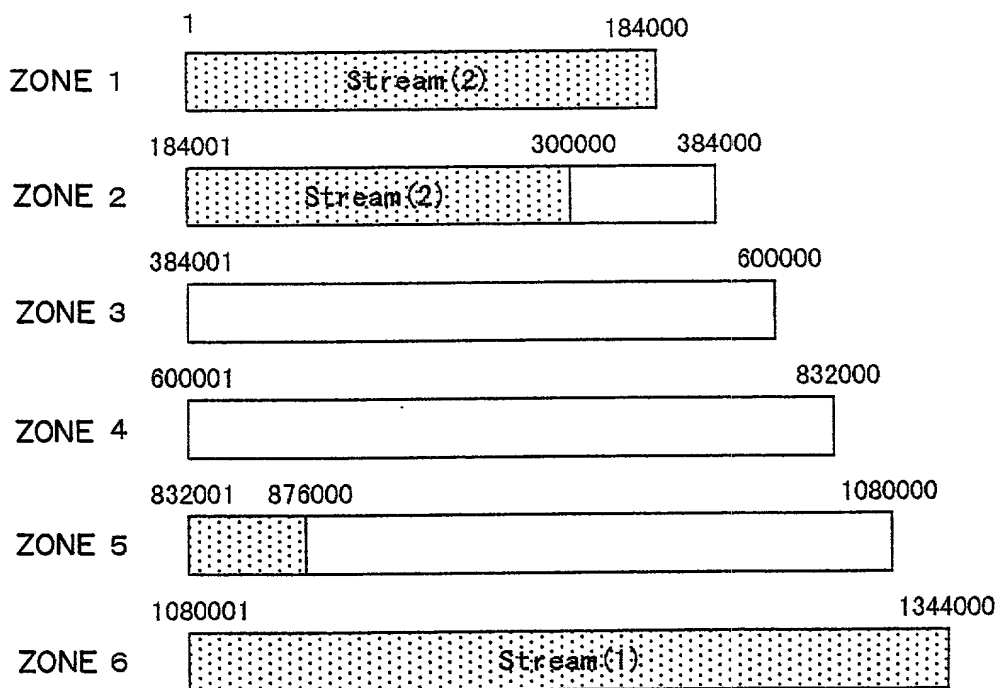


FIG. 47

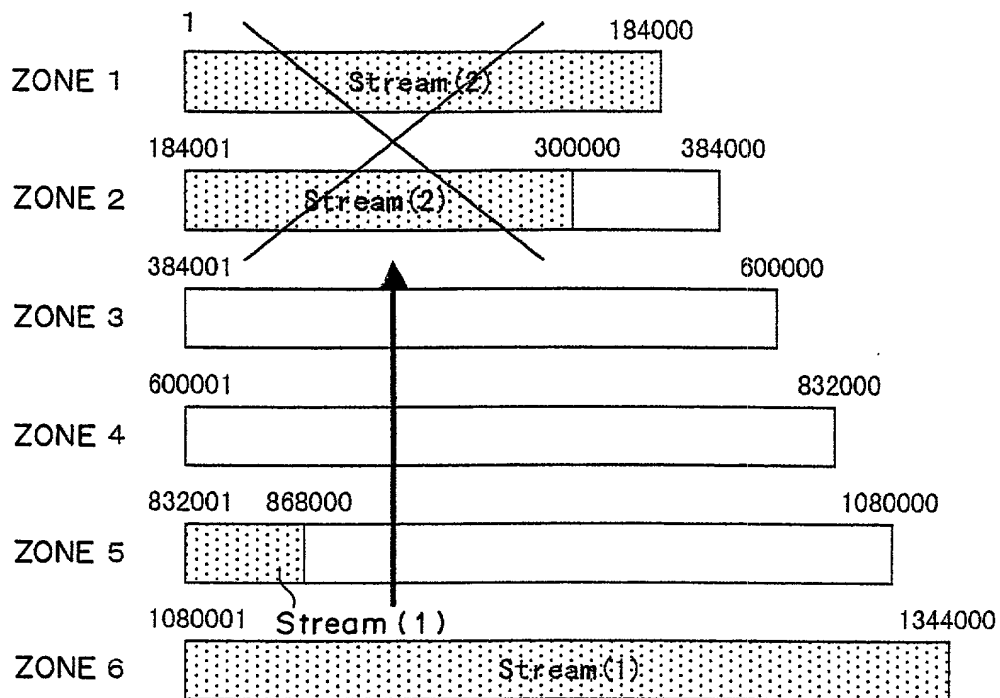


FIG. 48

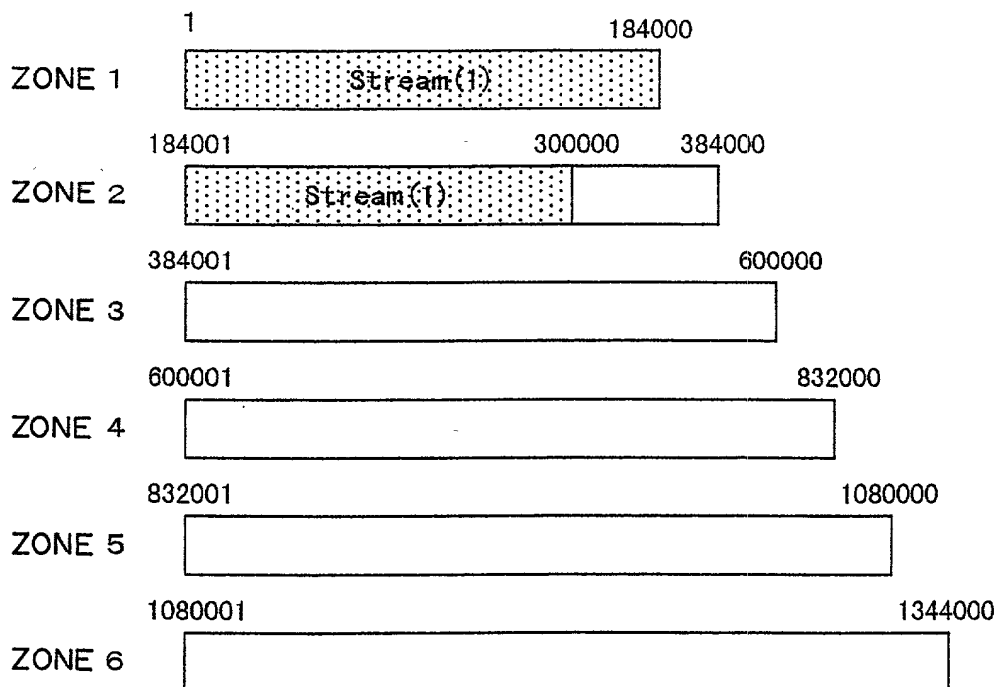


FIG. 49

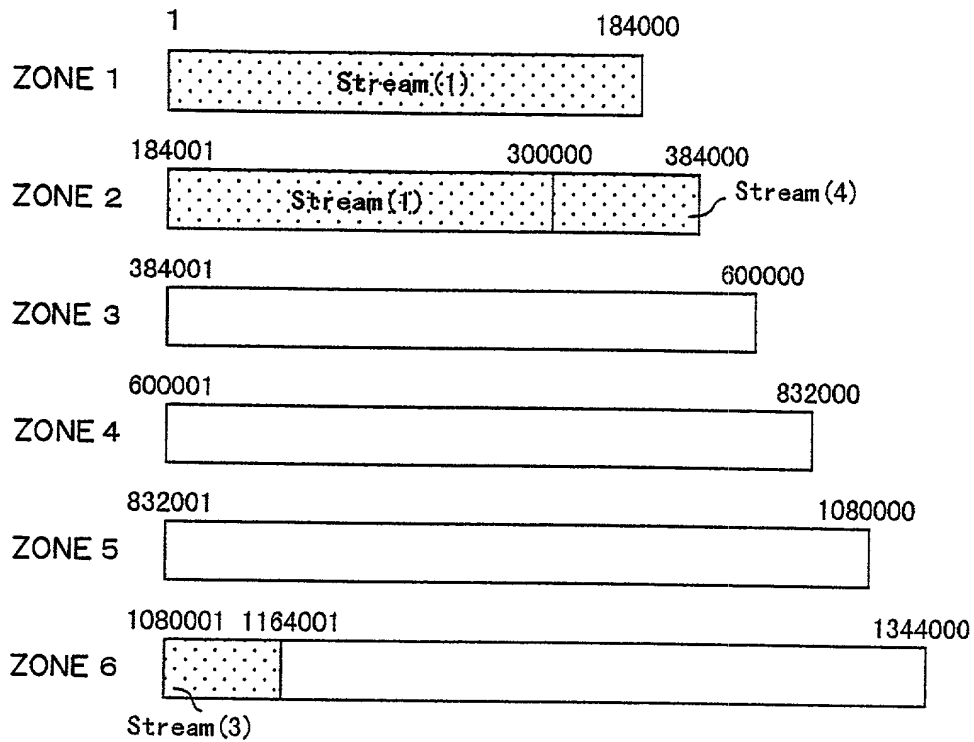


FIG. 50

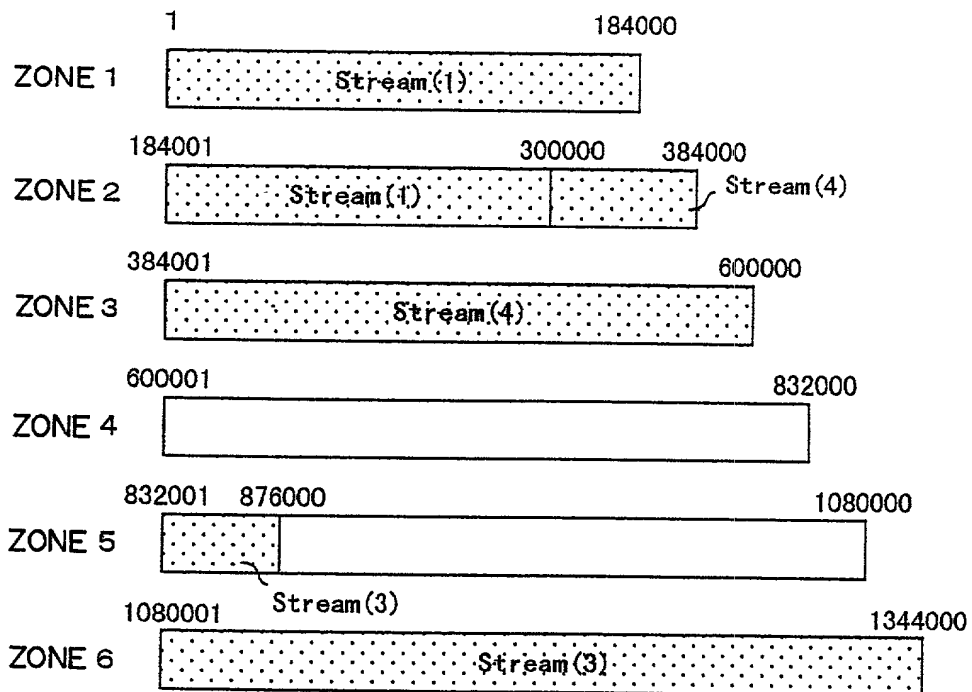


FIG. 51

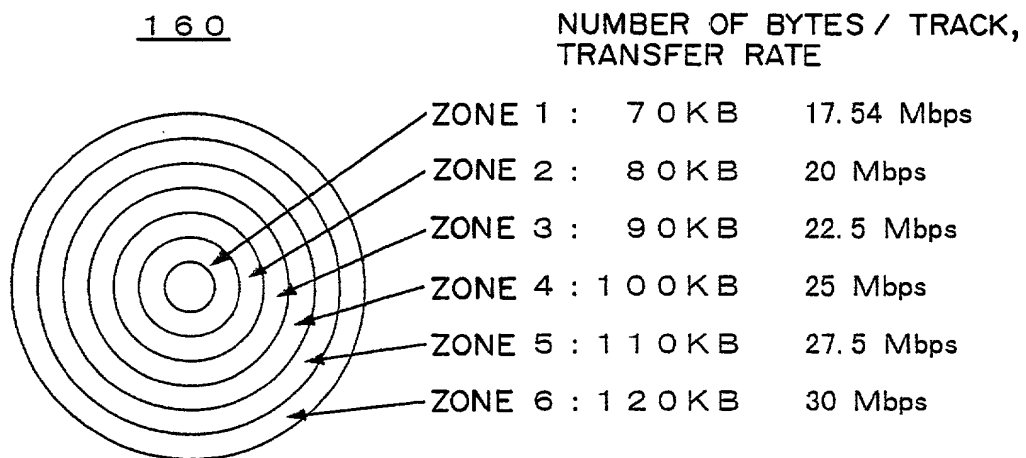
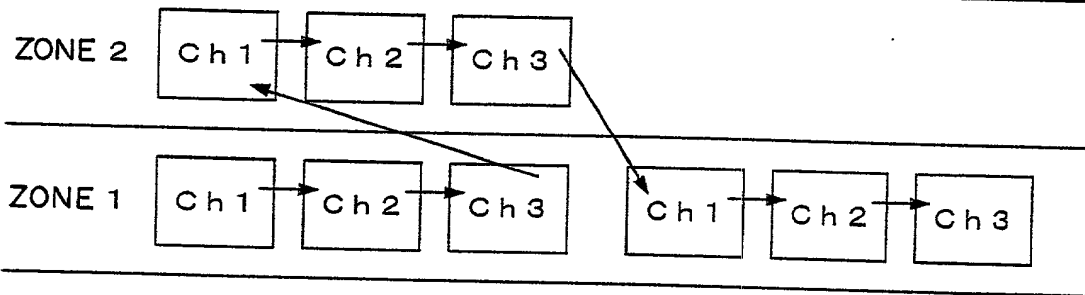


FIG. 52

STEP 1 : DISTRIBUTING AND RECORDING
DATA IN ZONES 1 AND 2

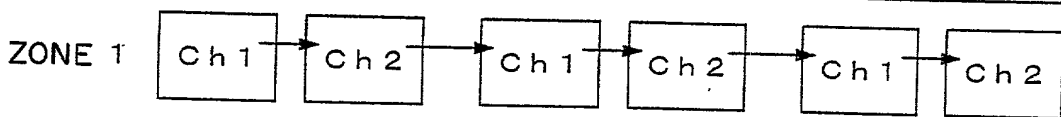
$$\leftarrow (17.5+20)/2 > 18$$



(a)

STEP 2 : RECORDING

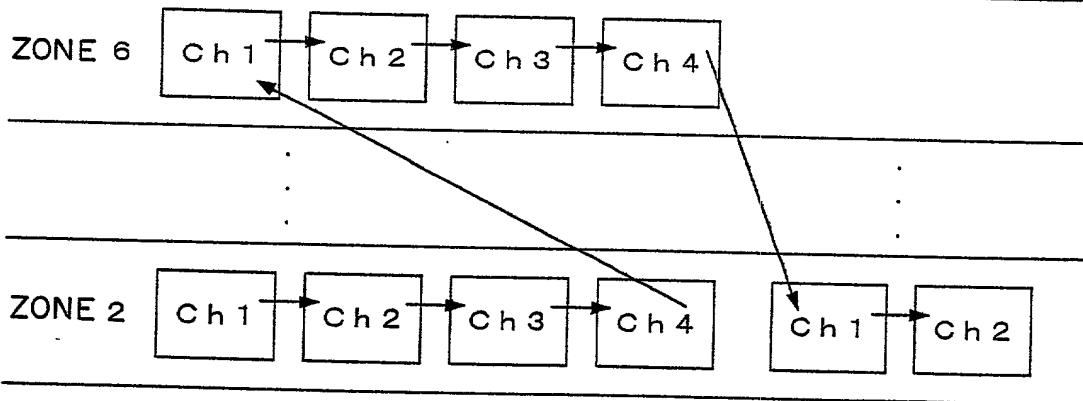
DATA IN ZONE 1 $\leftarrow 17.5 > 12$



(b)

STEP 3 : DISTRIBUTING AND RECORDING
DATA IN ZONES 2 AND 6

$$\leftarrow (20+30)/2 > 24$$

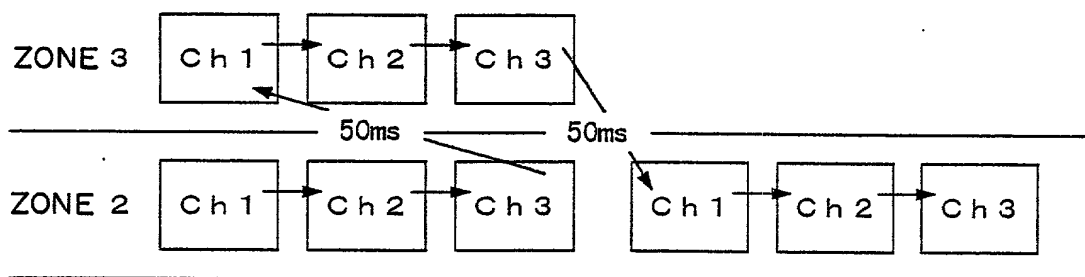


(c)

FIG. 53

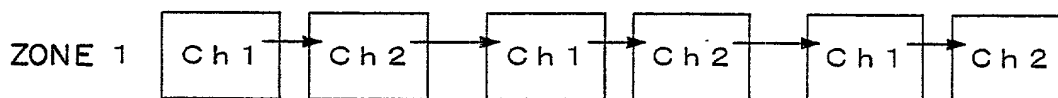
STEP 1 : DISTRIBUTING AND RECORDING
DATA IN ZONES 2 AND 3

$$\leftarrow (20+22.5) \times 0.9/2 > 18$$



(a)

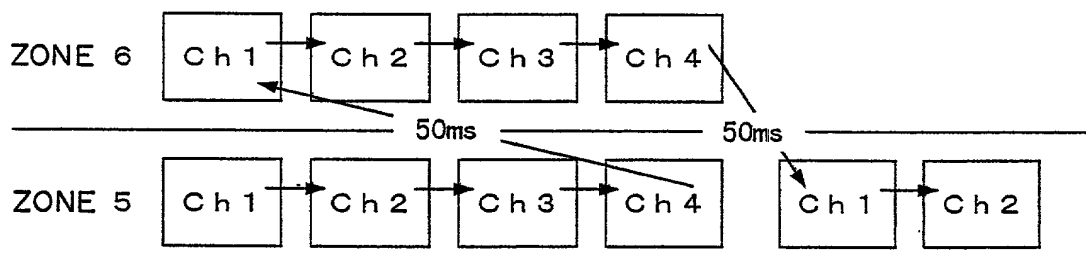
STEP 2 : RECORDING
DATA IN ZONE 1 $\leftarrow 17.5 > 12$



(b)

STEP 3 : DISTRIBUTING AND RECORDING
DATA IN ZONE 5 AND 6

$$\leftarrow (27.5+30) \times 0.9/2 > 24$$



(c)

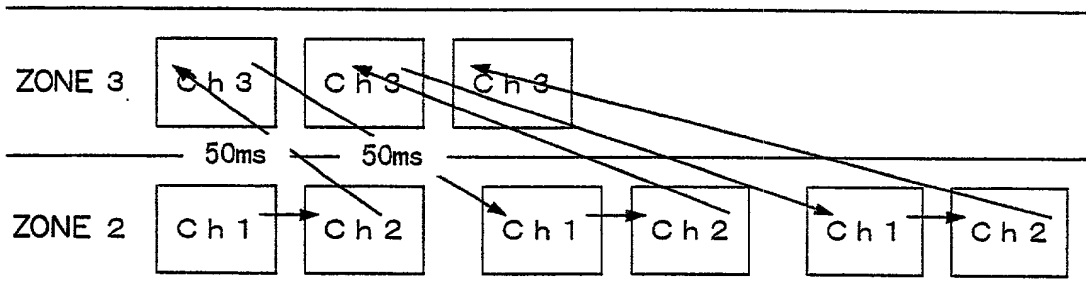
FIG. 54

STEP 1 : DISTRIBUTING AND RECORDING

DATA IN ZONES 2 AND 3

$$\leftarrow (20 + 2/3 + 22.5 \times 1/3) \times 0.9 > 18$$

DISTRIBUTING DATA ALTERNATELY THROUGH 1ch AND 2ch

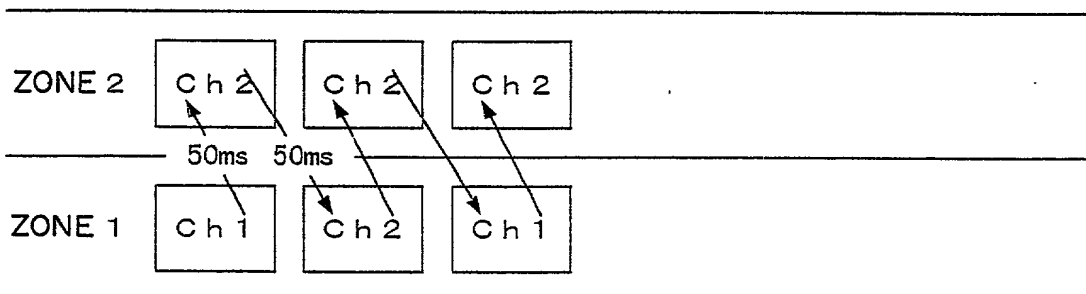


(a)

STEP 2 : RECORDING DATA

IN ZONES 1 AND 2

$$\leftarrow 17.5/2 + 20/2 \times 0.9 > 12$$



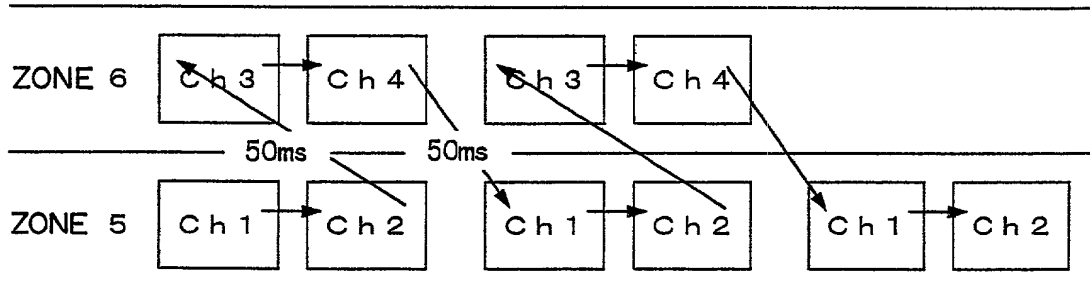
(b)

STEP 3 : DISTRIBUTING AND RECORDING

DATA IN ZONES 5 AND 6

$$\leftarrow (27.5 \times 2/4 + 30 \times 2/4) \times 0.9 > 24$$

ALTERNATELY DISTRIBUTING DATA IN TWO CHANNEL UNITS



(c)

FIG. 55

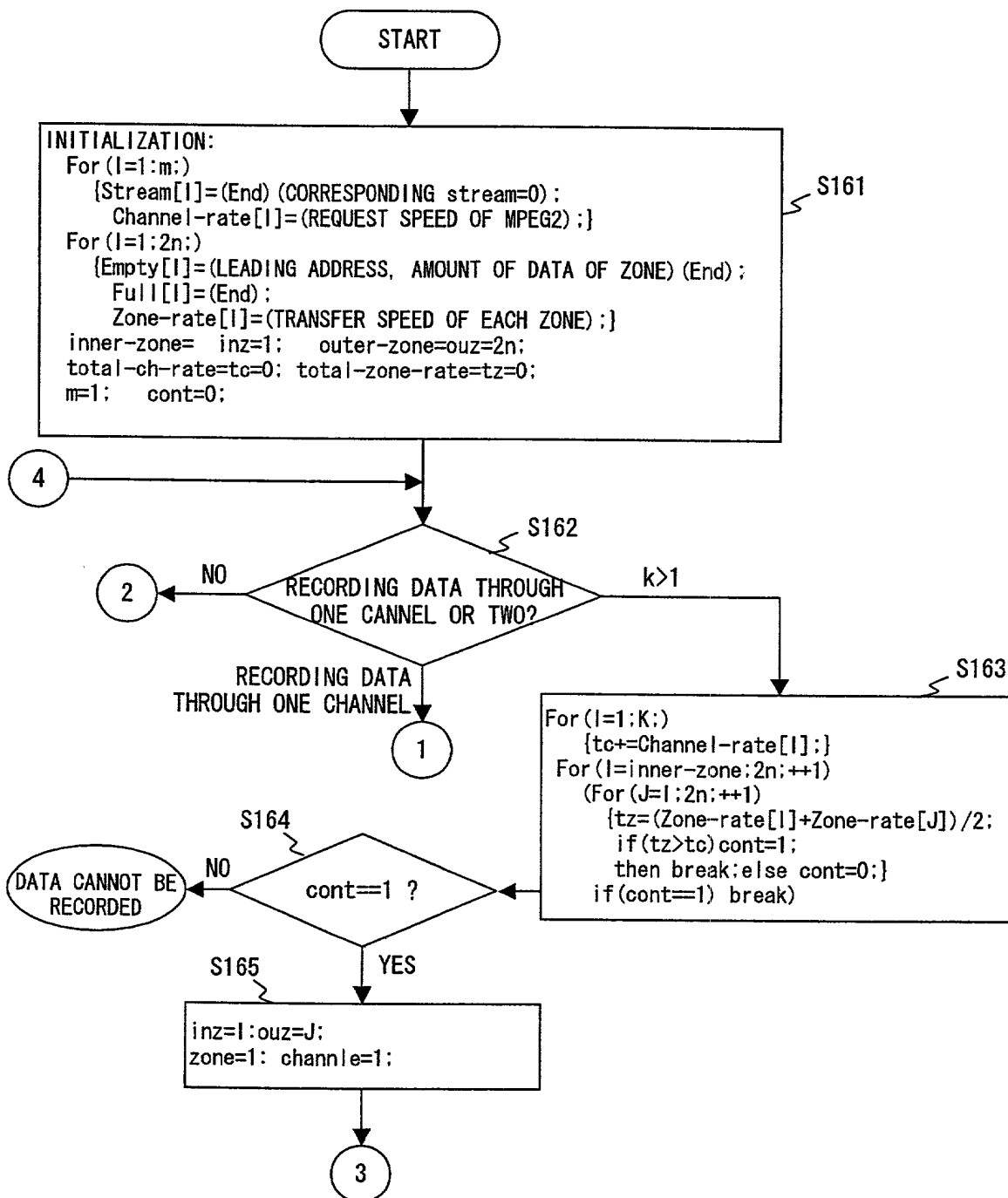


FIG. 56

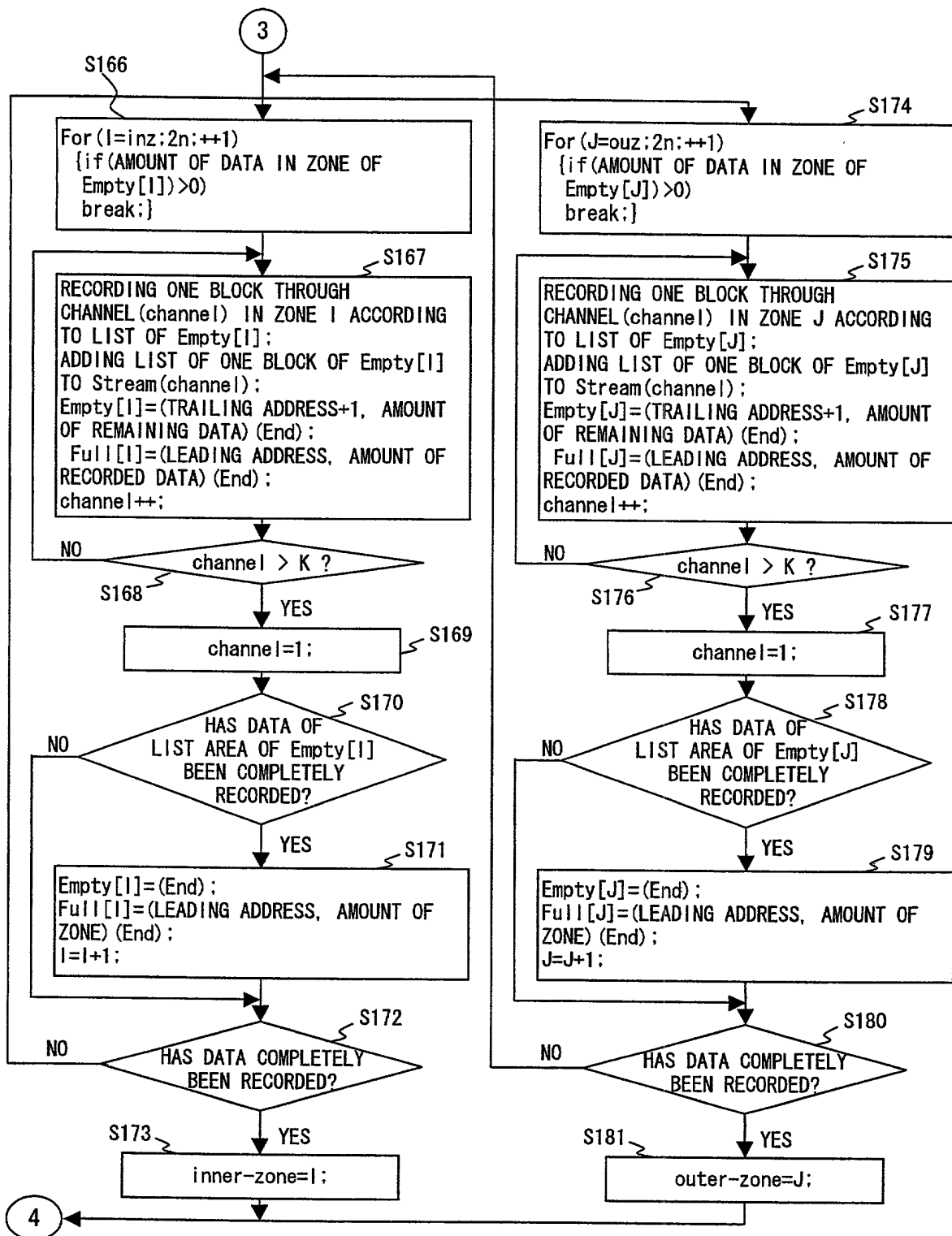


FIG. 57

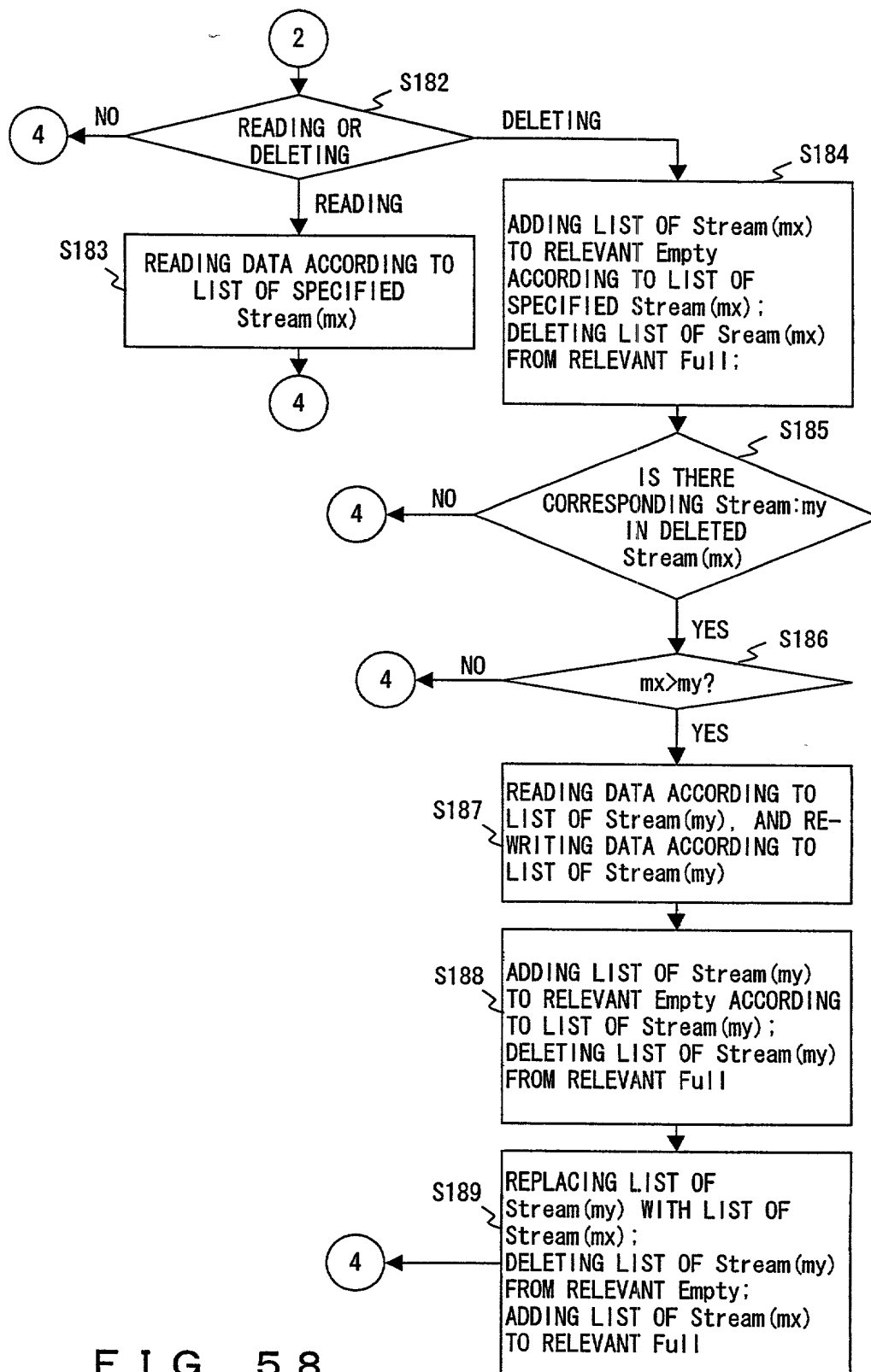


FIG. 58

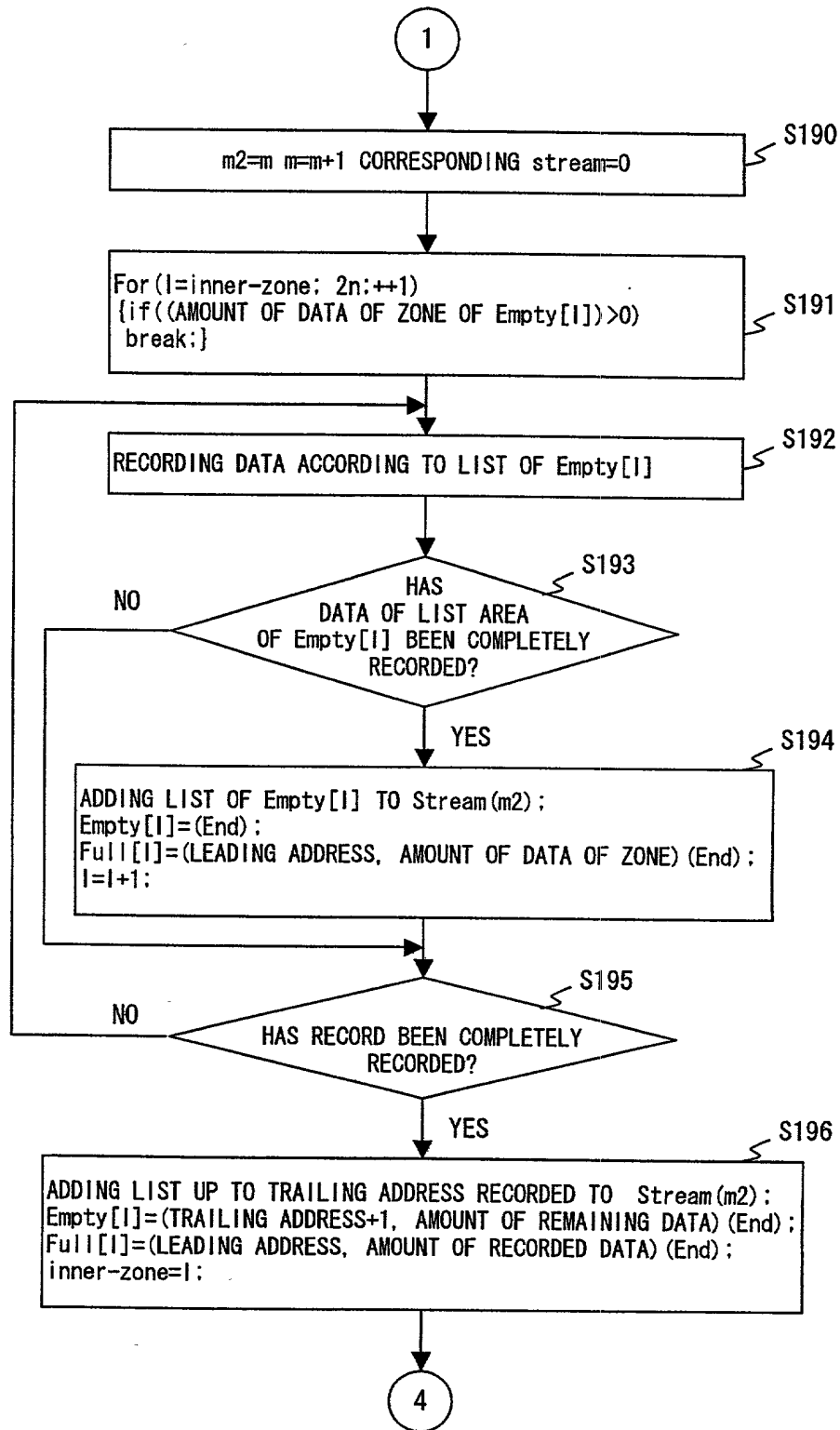


FIG. 59

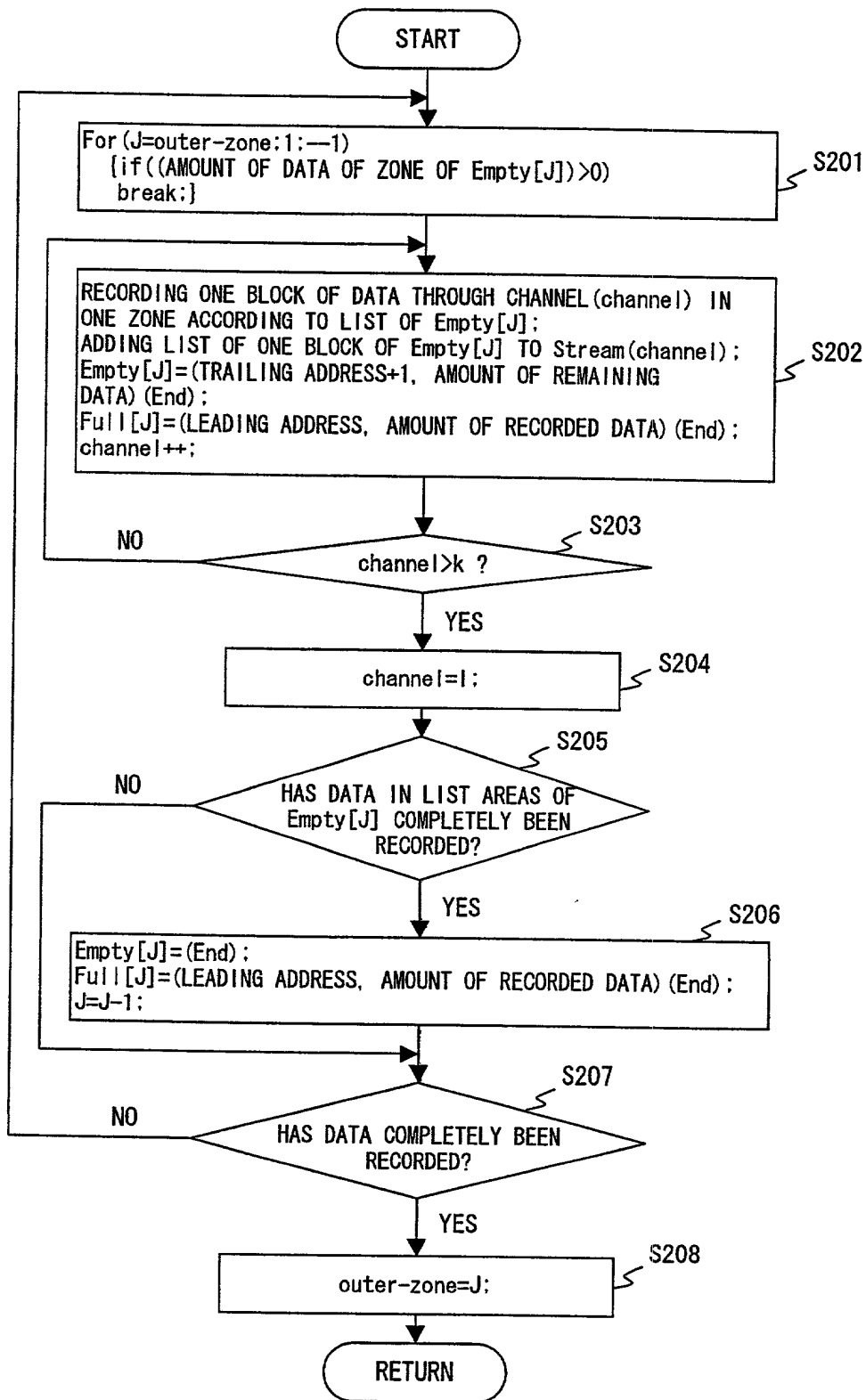


FIG. 60